

Fault-Tolerant Voting in a Simply-Typed Lambda Calculus

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Abstract

In this paper we present a translation from the simply typed lambda calculus into an extended simply typed lambda calculus that enables programs to be tolerant to transient faults. The translation triples all basic instructions and generates majority voting code for ensuring correct control-flow.

We demonstrate that the generated voting code causes evaluation to proceed successfully even in the presence of a transient fault corrupting a register other than a control flow register (i.e., the stack pointer or the instruction pointer).

Categories and Subject Descriptors D.1 [Programming Techniques]: Applicative (Functional) Programming; D.3.1 [Programming Languages]: Formal Definitions and Theory—Semantics

Keywords Transient Faults, Lambda Calculus, Compilation, Optimization

1. Introduction

A transient fault occurs when an energetic ray or a particle cause a bit-flip in the central processing unit, causing a change in the content of a register or an error in the instruction logic causing the result register of an instruction to become semantically wrong.

Transient faults (also called soft errors) are arguably becoming a larger and larger problem for computing. When performance of microprocessors increase with a growing number of transistors per square millimeter, processors are also becoming more volatile to transient faults caused by various kinds of radiation (alpha particles, neutrons, and cosmic rays) [4, 14]. Much work has focused on using software-only techniques and hardware/software hybrid techniques for fault detection and fault tolerance for transient faults (see [14, 13] for some good overviews), but only recently have correctness properties of the techniques been investigated formally [18, 8].

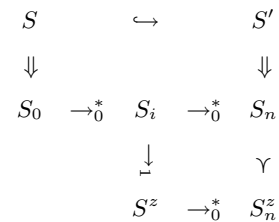
In previous work, Walker et al. [18] have given a translation from the simply typed lambda calculus into λ_{zap} , an extended lambda calculus that uses majority voting to guarantee well-typed λ_{zap} programs to execute correctly, even in the presence of a transient fault. The translation essentially triples all basic operations and uses majority voting for operations that may change the control-flow of the program. An essential part of the dynamic semantics of Walker et al.'s faulty lambda calculus is the primitive voting operation, which is used to guarantee correct control-flow behaviour, for function calls and conditionals, in the presence of

transient faults. The voting operation is trusted in the sense that Walker et al.'s faulty lambda calculus does not consider the effects of transient faults during voting. This setup suggests that special hardware support may be needed for voting.

The work by Walker et al. on the faulty lambda calculus assumes the Single Event Upset (SEU) fault model, which suggests that at most one fault occurs during evaluation. In this work, we adopt the SEU fault model and extend Walker et al.'s work by giving a translation to a refined lambda calculus that does not provide a built-in voting mechanism, but rather provides two simple operations that are sufficient for compiling away voting in the translation and are *atomic* in the sense that it is assumed that no transient faults corrupt the operations. Intuitively, the first atomic operation allocates a function closure and stores the closure address in three distinct registers. The second atomic operation checks atomically whether two addresses to closures are identical and if so calls the function denoted by the closure. In case the addresses are different, an alternative expression is evaluated.

The two atomic operations make explicit the assumptions of our approach. Whereas the first atomic operation can be implemented atomically on traditional architectures, using a few testing and fault-correcting operations, special architectural support is needed for the second atomic operation for 100 percent safety with respect to the SEU fault model. Without special architectural support, a transient fault on the comparison flag or on the target address during the comparison and branch code sequence can pass non-detected. However, making the assumptions explicit makes it straightforward to reason about the probability of a non-detected (and non-corrected) transient fault.

The contributions of our work is illustrated with the following diagram:



First assume that S is a source language machine state that evaluates in one step into a source language machine state S' . Then translating S into the target language equipped with atomic operations for fault-tolerant computing yields S_0 that, in the presence of no faults (indicated with the 0 on the relation $\xrightarrow{*}_0$), evaluates in zero or more steps into the machine state S_n , which is also the result of translating S' . Now, if at any point during evaluation of S_0 to S_n , say at machine state S_i , a transient fault occurs (indicated with the 1 on the relation $\xrightarrow{*}_1$), which will render the system in a machine state S^z (z for zap), then evaluation can proceed to a machine state S_n^z , which essentially contains enough information to extract the

machine state S' . In particular, if S' is essentially an integer value d then a majority voting applied to S'_n will result in the integer d .

Whereas the faulty translation we propose is similar to that of Walker et al. [18] in that it triples all basic operations, the translation differs in the way function application and conditional expressions (operations that may change control-flow) are translated. Instead of relying on the dynamic semantics of the target language to do the voting, our faulty translation generates code for the voting. Effectively, a conditional expression

```
if  $e$  then  $e_1$  else  $e_2$ 
```

is translated into the following code:

```
let  $\langle x_1, x_2, x_3 \rangle = e'$ 
in if  $x_1$  then if  $x_2$  then  $e'_1$ 
      else if  $x_3$  then  $e'_1$  else  $e'_2$ 
  else if  $x_2$  then if  $x_3$  then  $e'_1$  else  $e'_2$ 
    else  $e'_2$ 
```

Here e' , e'_1 , and e'_2 are the results of translating e , e_1 , and e_2 , respectively, and the `let` construct binds the three results of evaluating the faulty translation of e . Notice that the code is safe up-to one fault in any of the values x_1 , x_2 , and x_3 ; if at most one fault has occurred, before or during voting, the correct branch is taken.

One artifact of the approach taken here is that the soundness proof relies heavily on the translation and not only on the type system for the target language. Because a fault can occur at any step in the dynamic semantics of the target program, the soundness proof has to consider all possibilities, even faults occurring during voting. It is an open problem how to capture correctness of generated voting code in a type system (i.e., that any single fault will lead to correct results).

In the present setup, the technique assumes fault-safe control registers for holding the instruction pointer, the stack pointer, and the environment for the current closure. In this respect, the setup here is not different from that of [18], from which we have borrowed the particular machine state model.

For self-containedness and clarity, we have chosen to present the technique for incorporating voting in the generated code as a translation from a standard simply typed lambda calculus to an extended simply typed lambda calculus with functions that take multiple arguments and return multiple values and atomic primitives for implementing voting. Another equivalent approach would be to use Walker et al.'s λ_{zap} [18] as the source language for a translation into our extended language and show that the built-in voting primitives in the λ_{zap} language can be eliminated and translated into equivalent voting code in the extended language.

1.1 Outline

In Section 1.2, we first give an overview of related work.

In Section 2, we briefly present the source language of the translation, a simply typed lambda calculus with booleans and integers. We provide a dynamic contextual small-step semantics as well as a type system for the language and state a type soundness result saying that “well-typed programs do not get stuck”.

In Section 3, we present an extended lambda calculus, which supports functions that take multiple arguments and return multiple results. The language also supports two new atomic operations that are sufficient for incorporating in-lined voting.

In Section 4, we present a translation from the source language to the extended language and prove that the result of the translation is sound with respect to the semantics of the source language and that a single transient fault during execution of an extended language program results in three values for which two of the values agree with the result of the non-faulty semantics of the source language program.

Finally, in Section 5, we describe future work and conclude.

1.2 Related Work

Most related to this work is the work by Walker et al. on which the present work builds [18]. Whereas Walker et al. focus on deriving a type system for the intermediate language, the focus in this paper is on generating fault-tolerant code for in-lined voting and to establish a soundness result that expresses that in-lined voting code works correctly in the presence of any single transient fault occurring during evaluation.

Also very much related to our work is the work by Perry et al. on a fault-tolerant typed assembly language [8], a type-based framework for ensuring that transient faults in the processor do not go undetected. Whereas the work on a typed assembly language for fault-tolerance seems very promising, their framework does not provide full fault-tolerance in the sense that programs are guaranteed to operate correctly even in the presence of a single fault. For such a guarantee, their framework would be required to incorporate some kind of voting mechanism. A delicate issue in using a typed assembly language for our compilation of voting, would be to support, in the type system, propagation of comparison results in such a way that appropriate (sound) voting code would be enforced by the type system.

Several previous approaches to software-implemented fault detection and fault tolerance have been covered in the literature [9, 10, 12, 11, 13, 5].

In [9], Rebaudengo et al. make use of data duplication and insertion of control-flow check code to harden software by detecting data faults and control-flow faults (faults occurring during execution of conditional statements and jumps between basic blocks). In [10], Rebaudengo et al. extend their previous work by using data duplication and check-sums to recover from transient data faults.

None of the above mentioned approaches, except the approach taken in [18], have been verified formally in terms of correctness or soundness results.

Another line of related work is work on policy based techniques, such as software fault isolation (SFI) [17, 6], control-flow integrity (CFI) [1, 2], and hybrids [16, 15]. None of these techniques, however, protect against transient faults occurring immediately after evaluation of protection code (i.e., check code or sand-boxing code) or against faults occurring within the boundaries of the sand-boxing or during execution of sand-boxing code.

At a technical level, the work here is also related to the work by Yoshida et al. on logical reasoning for higher-order functions with local state [19]. In our work (and in the work by Walker et al. [18]), machine states $(M; e)$, where M maps labels to values and e is an expression, are considered identical up-to consistent renaming of labels. In the work by Yoshida et al., the same equalities are assured by extending machine states to be on the form $\nu \tilde{l}.(M; e)$, where the first part existentially quantifies over the label set \tilde{l} to provide implicit renaming. Whereas we need the property to enforce only one kind of non-determinacy in the dynamic semantics (fault introduction), Yoshida et al. also use the property to ease the reasoning about when two states are observationally equivalent; programs that differ only in the order memory is allocated should result in identical machine states.

2. The Source Language

We assume a denumerable infinite set of labels, ranged over by l , and a denumerable infinite set of variables, ranged over by x . The grammar for source language values and source language expres-

sions is given as follows:

$$\begin{aligned}
v &::= l \mid \mathbf{true*} \mid \mathbf{false*} \mid n* \\
e &::= x \mid v \mid n \mid \mathbf{true} \mid \mathbf{false} \\
&\quad \mid \lambda x.e \mid e_1 e_2 \\
&\quad \mid \mathbf{if } e \mathbf{ then } e_1 \mathbf{ else } e_2 \mid \mathbf{let } x = e_1 \mathbf{ in } e_2 \\
&\quad \mid e_1 \mathit{op} e_2 \quad \mathit{op} \in \{+, <\}
\end{aligned}$$

Notice that the grammar distinguishes between values (e.g., $\mathbf{true*}$) and expressions evaluating to a value in one step (e.g., \mathbf{true}).

2.1 Source Language Dynamic Semantics

For the source language, *evaluation contexts*, ranged over by E , take the following form:

$$\begin{aligned}
E &::= [\cdot] \mid E e \mid v E \mid \mathbf{let } x = E \mathbf{ in } e \\
&\quad \mid \mathbf{if } E \mathbf{ then } e_1 \mathbf{ else } e_2 \\
&\quad \mid E \mathit{op} e \mid v \mathit{op} E
\end{aligned}$$

When E is an evaluation context and e is an expression, we write $E[e]$ to denote the expression resulting from filling the hole in E with e .

Memory maps, ranged over by M , map labels to lambda expressions

$$M ::= \varepsilon \mid M, l \mapsto \lambda x.e \quad l \notin \text{Dom}(M)$$

Machine states, ranged over by S , are pairs $(M; e)$ of a memory map M and an expression e . A *terminal* machine state, S_v , is a machine state of the form $(M; v)$, where M is a memory map and v is a value. Single-step reductions take the form $S \hookrightarrow S'$, where S and S' are machine states. We consider machine states identical up-to consistent renaming of labels, which makes allocation in the dynamic semantics deterministic; we shall discuss this issue later in Section 4.5.

Small Step Reductions

$$\boxed{M; e \hookrightarrow M'; e'}$$

$$\frac{M; e \hookrightarrow M'; e' \quad E \neq [\cdot]}{M; E[e] \hookrightarrow M'; E[e']} \quad (1)$$

$$\frac{}{M; \lambda x.e \hookrightarrow (M, l \mapsto \lambda x.e); l} \quad (2)$$

$$\frac{M(l) = \lambda x.e}{M; l v \hookrightarrow M; e[v/x]} \quad (3)$$

$$\frac{}{M; \mathbf{if } \mathbf{true*} \mathbf{ then } e_1 \mathbf{ else } e_2 \hookrightarrow M; e_1} \quad (4)$$

$$\frac{v \neq \mathbf{true*}}{M; \mathbf{if } v \mathbf{ then } e_1 \mathbf{ else } e_2 \hookrightarrow M; e_2} \quad (5)$$

$$\frac{}{M; \mathbf{false} \hookrightarrow M; \mathbf{false*}} \quad (6)$$

$$\frac{}{M; \mathbf{true} \hookrightarrow M; \mathbf{true*}} \quad (7)$$

$$\frac{}{M; n \hookrightarrow M; n*} \quad (8)$$

$$\frac{}{M; \mathbf{let } x = v \mathbf{ in } e \hookrightarrow M; e[v/x]} \quad (9)$$

$$\frac{n = n_1 + n_2}{M; n_1* + n_2* \hookrightarrow M; n*} \quad (10)$$

$$\frac{n_1 < n_2}{M; n_1* < n_2* \hookrightarrow M; \mathbf{true*}} \quad (11)$$

$$\frac{n_2 \leq n_1}{M; n_1* < n_2* \hookrightarrow M; \mathbf{false*}} \quad (12)$$

The transitive, reflexive closure of \hookrightarrow , written \hookrightarrow^* , is defined by the following two rules:

$$\frac{S \hookrightarrow S' \quad S' \hookrightarrow^* S''}{S \hookrightarrow^* S''} \quad (13)$$

$$\frac{}{S \hookrightarrow^* S} \quad (14)$$

We further define $S \uparrow$ to mean that there exists an infinite sequence $S \hookrightarrow S_1 \hookrightarrow S_2 \hookrightarrow \dots$. Moreover, when $S = (M; e)$ is some machine state and E is an evaluation context, we write $E[S]$ to mean $(M; E[e])$.

2.2 Source Language Type System

Types, ranged over by τ take the following form:

$$\tau ::= \mathit{bool} \mid \mathit{nat} \mid \tau \rightarrow \tau'$$

Type assumptions Γ map variables and labels to types:

$$\Gamma ::= \Gamma, x : \tau \mid \Gamma, l : \tau \mid \varepsilon$$

The type system allows inferences among sentences of the form $\Gamma \vdash^s e : \tau$, which are read: “under the assumptions Γ , the expression e has type τ .”

Typing Judgments for Expressions

$$\boxed{\Gamma \vdash^s e : \tau}$$

$$\frac{\Gamma(x) = \tau}{\Gamma \vdash^s x : \tau} \quad (15)$$

$$\frac{\Gamma(l) = \tau}{\Gamma \vdash^s l : \tau} \quad (16)$$

$$\frac{}{\Gamma \vdash^s \mathbf{true*} : \mathit{bool}} \quad (17)$$

$$\frac{}{\Gamma \vdash^s \mathbf{false*} : \mathit{bool}} \quad (18)$$

$$\frac{}{\Gamma \vdash^s \mathbf{true} : \mathit{bool}} \quad (19)$$

$$\frac{}{\Gamma \vdash^s \mathbf{false} : \mathit{bool}} \quad (20)$$

$$\frac{}{\Gamma \vdash^s n* : \mathit{nat}} \quad (21)$$

$$\frac{}{\Gamma \vdash^s n : \mathit{nat}} \quad (22)$$

$$\frac{\Gamma \vdash^s e : \mathit{bool} \quad \Gamma \vdash^s e_1 : \tau \quad \Gamma \vdash^s e_2 : \tau}{\Gamma \vdash^s \mathbf{if } e \mathbf{ then } e_1 \mathbf{ else } e_2 : \tau} \quad (23)$$

$$\frac{\Gamma \vdash^s e_1 : \tau \quad \Gamma, (x : \tau) \vdash^s e_2 : \tau'}{\Gamma \vdash^s \text{let } x = e_1 \text{ in } e_2 : \tau'} \quad (24)$$

$$\frac{\Gamma \vdash^s e_1 : \tau \rightarrow \tau' \quad \Gamma \vdash^s e_2 : \tau}{\Gamma \vdash^s e_1 e_2 : \tau'} \quad (25)$$

$$\frac{\Gamma, (x : \tau) \vdash e : \tau'}{\Gamma \vdash^s \lambda x. e : \tau \rightarrow \tau'} \quad (26)$$

$$\frac{\Gamma \vdash^s e_1 : \text{nat} \quad \Gamma \vdash^s e_2 : \text{nat}}{\Gamma \vdash^s e_1 < e_2 : \text{bool}} \quad (27)$$

$$\frac{\Gamma \vdash^s e_1 : \text{nat} \quad \Gamma \vdash^s e_2 : \text{nat}}{\Gamma \vdash^s e_1 + e_2 : \text{nat}} \quad (28)$$

Following the approach of [18], typing of machine states is defined by point-wise typing of memory maps.

Typing Judgments for Memory

$$\boxed{\vdash^s M : \Gamma}$$

$$\frac{\begin{array}{l} \text{Dom}(M) = \text{Dom}(\Gamma) \\ \text{for all } l \in \text{Dom}(M). \\ M(l) = \lambda x. e \\ \Gamma(l) = \tau \rightarrow \tau' \\ \Gamma, (x : \tau) \vdash^s e : \tau' \end{array}}{\vdash^s M : \Gamma} \quad (29)$$

The expression part e of a machine state $(M; e)$ is typed under the type assumptions provided by the memory map M .

Typing Judgments for Machine States

$$\boxed{\vdash^s (M; e) : \tau}$$

$$\frac{\vdash^s M : \Gamma \quad \Gamma \vdash^s e : \tau}{\vdash^s (M; e) : \tau} \quad (30)$$

2.3 Properties of the Source Language

We first give a few definitions before we present a unique decomposition proposition, which is used for the proofs of type preservation and progress [7].

A *redex*, ranged over by r , is an expression of the form

$$\begin{array}{l} r ::= n \mid l v \mid \text{let } x = v \text{ in } e \mid \lambda x. e \\ \mid \text{if } v \text{ then } e_1 \text{ else } e_2 \mid \text{true} \mid \text{false} \\ \mid v_1 \text{ op } v_2 \quad \text{op} \in \{+, <\} \end{array}$$

The following unique decomposition proposition states that any well-typed term is either a value or the term can be decomposed into a unique context and a unique well-typed redex. Moreover, filling the context with an expression of the same type as the redex results in a well-typed term:

PROPOSITION 1 (Unique Decomposition). *If $\vdash^s (M; e) : \tau$ then either e is a value v or there exists a unique E , a unique redex e' , and a unique τ' such that $e = E[e']$ and $\vdash^s (M; e') : \tau'$. Furthermore, for all e'' such that $\vdash^s (M; e'') : \tau'$, it follows that $\vdash^s (M; E[e'']) : \tau$.*

PROOF By induction over the derivation $\Gamma \vdash^s e : \tau$, where $\vdash^s M : \Gamma$. \square

The proofs of the following type preservation and progress propositions is then straightforward and standard [7].

PROPOSITION 2 (Type Preservation). *If $\vdash^s S : \tau$ and $S \hookrightarrow S'$ then $\vdash^s S' : \tau$.*

PROOF By induction over the structure of the expression-part of S , using Proposition 1. \square

PROPOSITION 3 (Progress). *If $\vdash^s S : \tau$ then either (1) S is a terminal machine state S_v ; or (2) there exists S' such that $S \hookrightarrow S'$.*

PROOF By induction over the structure of the expression-part of S , using Proposition 1. \square

PROPOSITION 4 (Type Soundness). *If $\vdash^s S : \tau$ then either $S \uparrow$ or there exists a terminal machine state S_v such that $S \hookrightarrow^* S_v$.*

PROOF By induction on the number of machine steps using Proposition 2 and Proposition 3. \square

3. The Extended Language

We now extend the source language to allow functions to take multiple arguments and return multiple results. We use c to range over a finite set of colors. We use vs to denote (colored) *value sequences* $\langle v_1 \triangleright c_1, \dots, v_n \triangleright c_n \rangle$, $n \geq 0$ and xs to denote (colored) *variable sequences* $\langle x_1 \triangleright c_1, \dots, x_n \triangleright c_n \rangle$, $n \geq 0$. Moreover, we use cs to denote *color sequences* $\langle c_1, \dots, c_n \rangle$, $n \geq 0$. Intuitively, colors are used in what follows to distinguish different copies of tripled computation.

We also extend the source language with two atomic operations that capture precisely the assumptions of the approach, as described in the introduction.

Intuitively, the first atomic operation $\lambda^{cs} xs. e$ simultaneously loads the same function label into a sequence of distinct colored registers, as specified by the color sequence cs . The source language lambda expression $\lambda x. e$ can be encoded in the extended language by the expression $\lambda^{cs} \langle x \rangle. e'$, where e' is the extended-language version of e and where cs is a singleton color sequence.

The second atomic operation **appe** $[l_1, l_2] a$ **else** e atomically checks if l_1 and l_2 are identical labels and if so calls the function denoted by l_1 and l_2 with a as argument. In case the labels are different, the expression e is evaluated instead.

Notice that atomicity here refers to the requirement that, for 100 percent soundness of the approach, the operation must operate faithfully even if a transient fault occurs during the operation.

The grammar for the extended language is as follows:

$$\begin{array}{l} v ::= l \mid \text{true}^* \mid \text{false}^* \mid n^* \\ w ::= v \triangleright c \\ vs ::= w \mid \langle w_1, \dots, w_n \rangle \quad n \geq 0 \\ cs ::= \langle c_1, \dots, c_n \rangle \quad n \geq 0 \\ xs ::= \langle x_1 \triangleright c_1, \dots, x_n \triangleright c_n \rangle \quad n \geq 0 \\ \text{op} ::= + \mid < \\ e ::= x \triangleright c \mid w \mid n \triangleright c \mid \text{true} \triangleright c \mid \text{false} \triangleright c \\ \mid \lambda^{cs} xs. e \mid \text{appe } [e_1, e_2] e \text{ else } e' \\ \mid e_1 e_2 \mid e \text{ op }_c e' \mid \langle e_1, \dots, e_n \rangle \quad n \geq 0 \\ \mid \text{if } e \text{ then } e_1 \text{ else } e_2 \mid \text{let } xs = e_1 \text{ in } e_2 \end{array}$$

Assuming $xs = \langle x_1 \triangleright c_1, \dots, x_n \triangleright c_n \rangle$ and $vs = \langle v_1 \triangleright c_1, \dots, v_n \triangleright c_n \rangle$ and provided e is an extended language expression, we define the *substitution of vs for xs in e* , written $e[vs/xs]$, as the simultaneous substitution of pairs $v_i \triangleright c_i$ for free occurrences of pairs $x_i \triangleright c_i$ in e , assuming $1 \leq i \leq n$. Notice that substitutions, according to the above definition, must be color preserving.

3.1 Dynamic Semantics

Evaluation contexts, ranged over by E , take the following form:

$$\begin{aligned}
E ::= & \quad [\cdot] \mid E e \mid w E \mid \mathbf{appe} [E, e] e' \mathbf{else} e'' \\
& \quad \mid \mathbf{appe} [w, E] e \mathbf{else} e' \mid \mathbf{appe} [w, w'] E \mathbf{else} e \\
& \quad \mid \mathbf{let} \, xs = E \mathbf{in} \, e \mid \mathbf{if} \, E \mathbf{then} \, e_1 \mathbf{else} \, e_2 \\
& \quad \mid E \mathit{op}_c e \mid w \mathit{op}_c E \\
& \quad \mid \langle w_1, \dots, w_n, E, e_1, \dots, e_m \rangle \quad n, m \geq 0
\end{aligned} \tag{42}$$

As for the source language, when E is an evaluation context and e is an expression, we write $E[e]$ to denote the expression resulting from filling the hole in E with e .

Memory maps, ranged over by M , map labels to lambda expressions

$$M ::= \varepsilon \mid M, l \mapsto \lambda xs. e \quad l \notin \text{Dom}(M)$$

As for the source language, *machine states*, ranged over by S , are pairs $(M; e)$ of a memory map M and an expression e . A *terminal* (extended language) machine state S_v is a machine state on the form $(M; vs)$, for some memory map M and value sequence vs . Single-step, possibly faulty, reductions take the form $S \rightarrow_k S'$, where k is either 0 or 1 (indicating whether a transient fault has occurred) and where S and S' are machine states. As for the source language, extended language machine states are considered identical up-to consistent renaming of labels. Treating machine states identical up-to consistent renaming of labels is essential for establishing appropriate fault tolerance properties; we shall come back to this issue in Section 4.5.

Small Step Reductions

$$\boxed{M; e \rightarrow_k M'; e'}$$

$$\frac{n = n_1 + n_2}{M; n_1 * \triangleright c_1 +_c n_2 * \triangleright c_2 \rightarrow_0 M; n * \triangleright c} \tag{31}$$

$$\frac{w_i \neq n \triangleright c', i \in \{1, 2\}}{M; w_1 +_c w_2 \rightarrow_0 M; v \triangleright c} \tag{32}$$

$$\frac{n_1 < n_2}{M; n_1 * \triangleright c_1 <_c n_2 * \triangleright c_2 \rightarrow_0 M; \mathbf{true} * \triangleright c} \tag{33}$$

$$\frac{n_2 \leq n_1}{M; n_1 * \triangleright c_1 <_c n_2 * \triangleright c_2 \rightarrow_0 M; \mathbf{false} * \triangleright c} \tag{34}$$

$$\frac{w_i \neq n \triangleright c', i \in \{1, 2\}}{M; w_1 <_c w_2 \rightarrow_0 M; v \triangleright c} \tag{35}$$

$$\frac{vs = \langle l_1 \triangleright c_1, \dots, l_n \triangleright c_n \rangle \quad l_i = l}{M; \lambda^{(c_1, \dots, c_n)} xs. e \rightarrow_0 (M, l \mapsto \lambda xs. e); vs} \tag{36}$$

$$\frac{M(l) = \lambda xs. e}{M; (l \triangleright c) vs \rightarrow_0 M; e[vs/xs]} \tag{37}$$

$$M; \mathbf{if} \, \mathbf{true} * \triangleright c \mathbf{then} \, e_1 \mathbf{else} \, e_2 \rightarrow_0 M; e_1 \tag{38}$$

$$\frac{w \neq \mathbf{true} * \triangleright c}{M; \mathbf{if} \, w \mathbf{then} \, e_1 \mathbf{else} \, e_2 \rightarrow_0 M; e_2} \tag{39}$$

$$M; \mathbf{false} \triangleright c \rightarrow_0 M; \mathbf{false} * \triangleright c \tag{40}$$

$$M; \mathbf{true} \triangleright c \rightarrow_0 M; \mathbf{true} * \triangleright c \tag{41}$$

$$\frac{}{M; n \triangleright c \rightarrow_0 M; n * \triangleright c} \tag{42}$$

$$\frac{}{M; \mathbf{let} \, xs = vs \mathbf{in} \, e \rightarrow_0 M; e[vs/xs]} \tag{43}$$

$$\frac{M(l) = \lambda xs. e}{M; \mathbf{appe} [l \triangleright c, l \triangleright c'] vs \mathbf{else} \, e' \rightarrow_0 M; e[vs/xs]} \tag{44}$$

$$\frac{v_1 \neq v_2}{M; \mathbf{appe} [v_1 \triangleright c, v_2 \triangleright c'] vs \mathbf{else} \, e \rightarrow_0 M; e} \tag{45}$$

$$\frac{M; e \rightarrow_k M'; e' \quad E \neq [\cdot]}{M; E[e] \rightarrow_k M'; E[e']} \tag{46}$$

$$\frac{}{M; E[v \triangleright c] \rightarrow_1 M; E[v' \triangleright c]} \tag{47}$$

Notice that (47) implements a Single Event Upset (SEU) model [18]; the rule allows for a single transient fault to alter a value on the stack or a value in a register.

Notice also that because substitutions must preserve colors, to apply (37), (43), and (44), it must first be established that vs and xs agree on colors.

Further notice that colors have no influence on the reductions and are only there for proving properties about reductions. An implementation may therefore erase colors from terms before execution.

The transitive, reflexive closure of \rightarrow_k , written \rightarrow_k^* , is defined by the following two rules:

$$\frac{S \rightarrow_j S' \quad S' \rightarrow_k^* S''}{S \rightarrow_{j+k}^* S''} \tag{48}$$

$$\frac{}{S \rightarrow_0^* S} \tag{49}$$

We further define $S \uparrow_k$ to mean that there exists an infinite sequence $S \rightarrow_k^* S_1 \leftrightarrow_0 S_2 \leftrightarrow_0 \dots$. As for the source language, when $S = (M; e)$ is some machine state and E is an evaluation context, we write $E[S]$ to mean $(M; E[e])$.

3.2 A Type System for the Faulty Language

We now present a simple type system for the extended language. The type system associates terms with colors and is similar to the type system for the faulty lambda calculus, developed in [18].

Types, ranged over by τ , and *type and color sequences* $\langle \tau_1 \triangleright c_1, \dots, \tau_n \triangleright c_n \rangle$, ranged over by κ , take the following form:

$$\begin{aligned}
\kappa ::= & \quad \langle \tau_1 \triangleright c_1, \dots, \tau_n \triangleright c_n \rangle \mid \tau \triangleright c \\
\tau ::= & \quad \mathbf{bool} \mid \mathbf{nat} \mid \kappa \rightarrow \kappa'
\end{aligned}$$

Type assumptions Γ map labels to types and variables to types and colors:

$$\Gamma ::= \Gamma, l : \tau \mid \Gamma, x : \tau \triangleright c \mid \varepsilon$$

The type system allows inferences among sentences of the form $\Gamma \vdash e : \kappa$, which are read: “under the assumptions Γ , the expression e has type and color sequence κ .”

We often write $(xs : \kappa)$ to denote the type assumptions $(x_1 : \tau_1 \triangleright c_1, \dots, x_n : \tau_n \triangleright c_n)$ when $xs = \langle x_1 \triangleright c_1, \dots, x_n \triangleright c_n \rangle$ and $\kappa = \langle \tau_1 \triangleright c_1, \dots, \tau_n \triangleright c_n \rangle$ for some colors c_1, \dots, c_n .

Typing Judgments for Expressions

$\boxed{\Gamma \vdash e : \kappa}$

$$\frac{\Gamma(x) = \tau \triangleright c}{\Gamma \vdash x \triangleright c : \tau \triangleright c} \quad (50)$$

$$\frac{\Gamma(l) = \tau}{\Gamma \vdash l \triangleright c : \tau \triangleright c} \quad (51)$$

$$\frac{}{\Gamma \vdash \mathbf{true} \triangleright c : \mathit{bool} \triangleright c} \quad (52)$$

$$\frac{}{\Gamma \vdash \mathbf{false} \triangleright c : \mathit{bool} \triangleright c} \quad (53)$$

$$\frac{}{\Gamma \vdash \mathbf{true} \triangleright c : \mathit{bool} \triangleright c} \quad (54)$$

$$\frac{}{\Gamma \vdash \mathbf{false} \triangleright c : \mathit{bool} \triangleright c} \quad (55)$$

$$\frac{}{\Gamma \vdash n \triangleright c : \mathit{nat} \triangleright c} \quad (56)$$

$$\frac{}{\Gamma \vdash n \triangleright c : \mathit{nat} \triangleright c} \quad (57)$$

$$\frac{\Gamma \vdash e : \mathit{bool} \quad \Gamma \vdash e_1 : \kappa \quad \Gamma \vdash e_2 : \kappa}{\Gamma \vdash \mathbf{if} \ e \ \mathbf{then} \ e_1 \ \mathbf{else} \ e_2 : \kappa} \quad (58)$$

$$\frac{\Gamma \vdash e_1 : \kappa \quad \Gamma, (xs : \kappa) \vdash e_2 : \kappa'}{\Gamma \vdash \mathbf{let} \ xs = e_1 \ \mathbf{in} \ e_2 : \kappa'} \quad (59)$$

$$\frac{\Gamma \vdash e_1 : \kappa \rightarrow \kappa' \triangleright c \quad \Gamma \vdash e_2 : \kappa}{\Gamma \vdash e_1 \ e_2 : \kappa'} \quad (60)$$

$$\frac{\Gamma \vdash e_1 : \kappa \rightarrow \kappa' \triangleright c_1 \quad \Gamma \vdash e_2 : \kappa \rightarrow \kappa' \triangleright c_2 \quad \Gamma \vdash e : \kappa \quad \Gamma \vdash e' : \kappa'}{\Gamma \vdash \mathbf{appe} \ [e_1, e_2] \ e \ \mathbf{else} \ e' : \kappa'} \quad (61)$$

$$\frac{\Gamma, (xs : \kappa) \vdash e : \kappa' \quad \tau_i = \kappa \rightarrow \kappa' \quad i = [1..n]}{\Gamma \vdash \lambda^{(c_1, \dots, c_n)} xs.e : \langle \tau_1 \triangleright c_1, \dots, \tau_n \triangleright c_n \rangle} \quad (62)$$

$$\frac{\Gamma \vdash e_i : \tau_i \triangleright c_i \quad i = [1..n]}{\Gamma \vdash \langle e_1, \dots, e_n \rangle : \langle \tau_1 \triangleright c_1, \dots, \tau_n \triangleright c_n \rangle} \quad (63)$$

$$\frac{\Gamma \vdash e_i : \mathit{nat} \triangleright c_i \quad i = [1, 2]}{\Gamma \vdash e_1 \ +_c \ e_2 : \mathit{nat} \triangleright c} \quad (64)$$

$$\frac{\Gamma \vdash e_i : \mathit{nat} \triangleright c_i \quad i = [1, 2]}{\Gamma \vdash e_1 \ <_c \ e_2 : \mathit{bool} \triangleright c} \quad (65)$$

Typing Judgments for Memory

$\boxed{\vdash M : \Gamma}$

$$\frac{\text{Dom}(M) = \text{Dom}(\Gamma) \quad \text{for all } l \in \text{Dom}(M). \quad M(l) = \lambda xs.e \quad \Gamma(l) = \kappa \rightarrow \kappa' \quad \Gamma, (xs : \kappa) \vdash e : \kappa'}{\vdash M : \Gamma} \quad (66)$$

Typing Judgments for Machine States

$\boxed{\vdash (M; e) : \kappa}$

$$\frac{\vdash M : \Gamma \quad \Gamma \vdash e : \kappa}{\vdash (M; e) : \kappa} \quad (67)$$

3.3 Properties of the Extended Language

We now carry over the results of Section 2.3 to the extended language.

A *redex*, ranged over by r , is an expression of the form

$$\begin{aligned} r ::= & n \triangleright c \mid w \ vs \mid \mathbf{let} \ xs = vs \ \mathbf{in} \ e \mid \lambda^{cs} xs.e \\ & \mid \mathbf{if} \ v \triangleright c \ \mathbf{then} \ e_1 \ \mathbf{else} \ e_2 \mid \mathbf{true} \triangleright c \mid \mathbf{false} \triangleright c \\ & \mid w_1 \ op_c \ w_2 \quad op \in \{+, <\} \\ & \mid \mathbf{appe} \ [w_1, w_2] \ vs \ \mathbf{else} \ e \end{aligned}$$

PROPOSITION 5 (Unique Decomposition). *If $\vdash (M; e) : \kappa$ then either e is a value sequence vs or there exists a unique E , a unique redex e' , and a unique τ' such that $e = E[e']$ and $\vdash (M; e') : \tau'$. Furthermore, for all e'' such that $\vdash (M; e'') : \tau'$, it follows that $\vdash (M; E[e'']) : \kappa$.*

PROOF By induction over the derivation $\Gamma \vdash e : \kappa$, where $\vdash M : \Gamma$. \square

PROPOSITION 6 (Type Preservation). *If $\vdash S : \kappa$ and $S \rightarrow_0 S'$ then $\vdash S' : \kappa$*

PROOF By induction over the structure of the expression-part of S , using Proposition 5. \square

PROPOSITION 7 (Progress). *If $\vdash S : \kappa$ then either (1) S is a terminal machine state S_v ; or (2) there exists S' such that $S \rightarrow_0 S'$.*

PROOF By induction over the structure of the expression-part of S , using Proposition 5. \square

PROPOSITION 8 (Type Soundness). *If $\vdash S : \tau$ then either $S \uparrow_0$ or there exists a terminal machine state S_v such that $S \rightarrow_0^* S_v$.*

PROOF By induction on the number of machine steps using Proposition 6 and Proposition 7. \square

The following proposition expresses that evaluation is deterministic, which depends crucially on the fact that machine states are considered identical up-to consistent renaming of labels.

PROPOSITION 9 (Evaluation is Deterministic). *If $S \rightarrow_0 S'$ and $S \rightarrow_0 S''$ then $S = S''$.*

PROOF By induction on the structure of e in S . \square

This proposition is used later in the proof of the fault-tolerance property.

4. The Faulty Translation

A *translation environment* (ρ) is a mapping from identifiers to triples of identifiers:

$$\rho ::= \rho', x \mapsto (x_1, x_2, x_3) \quad \begin{aligned} & x \notin \text{Dom}(\rho') \wedge \\ & x_i = x_j \Rightarrow i = j \wedge \\ & x_i \notin \text{Ran}(\rho') \end{aligned}$$

$\mid \varepsilon$

The side conditions on valid translation environments are enforced to avoid variable capture.

The faulty translation is defined inductively over source language expressions as a function $\llbracket e \rrbracket \rho = e'$, where e is a source

$$\begin{aligned}
\llbracket x \rrbracket \rho &= \langle\langle x_1, x_2, x_3 \rangle\rangle \\
&\text{where } \rho(x) = (x_1, x_2, x_3) \\
\llbracket \lambda x. e \rrbracket \rho &= \lambda^{\langle r, g, b \rangle} \langle\langle x_1, x_2, x_3 \rangle\rangle. e' \\
&\text{where } e' = \llbracket e \rrbracket (\rho, x \mapsto (x_1, x_2, x_3)) \\
\llbracket n \rrbracket \rho &= \langle\langle n, n, n \rangle\rangle \\
\llbracket \text{true} \rrbracket \rho &= \langle\langle \text{true}, \text{true}, \text{true} \rangle\rangle \\
\llbracket \text{false} \rrbracket \rho &= \langle\langle \text{false}, \text{false}, \text{false} \rangle\rangle \\
\llbracket \text{let } x = e_1 \text{ in } e_2 \rrbracket \rho &= \\
&\text{let } \langle\langle x_1, x_2, x_3 \rangle\rangle = \llbracket e_1 \rrbracket \rho \\
&\text{in } \llbracket e_2 \rrbracket (\rho, x \mapsto (x_1, x_2, x_3)) \\
\llbracket \text{if } e \text{ then } e_1 \text{ else } e_2 \rrbracket \rho &= \\
&\text{let } \langle\langle x_1, x_2, x_3 \rangle\rangle = \llbracket e \rrbracket \rho \\
&\text{in if } x_1 \triangleright r \text{ then} \\
&\quad \text{if } x_2 \triangleright g \text{ then } \llbracket e_1 \rrbracket \rho \\
&\quad \text{else if } x_3 \triangleright b \text{ then } \llbracket e_1 \rrbracket \rho \text{ else } \llbracket e_2 \rrbracket \rho \\
&\text{else} \\
&\quad \text{if } x_2 \triangleright g \text{ then if } x_3 \triangleright b \text{ then } \llbracket e_1 \rrbracket \rho \text{ else } \llbracket e_2 \rrbracket \rho \\
&\quad \text{else } \llbracket e_2 \rrbracket \rho \\
\llbracket e_1 e_2 \rrbracket \rho &= \\
&\text{let } \langle\langle x_1, x_2, x_3 \rangle\rangle = \llbracket e_1 \rrbracket \rho \\
&\text{in } \text{appe } [x_1 \triangleright r, x_2 \triangleright g] (\llbracket e_2 \rrbracket \rho) \\
&\quad \text{else } \text{appe } [x_1 \triangleright r, x_3 \triangleright b] (\llbracket e_2 \rrbracket \rho) \\
&\quad \text{else } (x_2 \triangleright g) (\llbracket e_2 \rrbracket \rho) \\
\llbracket e_1 \text{ op } e_2 \rrbracket \rho &= \quad \text{op} \in \{+, <\} \\
&\text{let } \langle\langle x_1, x_2, x_3 \rangle\rangle = \llbracket e_1 \rrbracket \rho \\
&\text{in } \text{let } \langle\langle y_1, y_2, y_3 \rangle\rangle = \llbracket e_2 \rrbracket \rho \\
&\quad \text{in } \langle x_1 \triangleright r \text{ op}_r y_1 \triangleright r, x_2 \triangleright g \text{ op}_g y_2 \triangleright g, x_3 \triangleright b \text{ op}_b y_3 \triangleright b \rangle \\
\llbracket v \rrbracket \rho &= \langle\langle v, v, v \rangle\rangle
\end{aligned}$$

Figure 1. Translation of Expressions and Values.

language expression, ρ is a translation environment, and where e' is the result of translating e . For the translation, we assume three colors r , g , and b . We often write $\langle\langle A, B, C \rangle\rangle$ to mean $\langle A \triangleright r, B \triangleright g, C \triangleright b \rangle$. The translation rules are presented in Figure 1.

Essentially, each primitive operation in the source program is translated into three operations in the target language, corresponding to the three different colors r , g , and b . Variable bindings are also tripled, which means that a single let-binding is translated into a let-binding construct in the target language, binding three distinct variables with different colors r , g , and b . Moreover, arguments to functions are tripled and so are return values from functions. Control-flow operations (i.e., function calls and conditional expressions) translate into code that involves voting. For conditional expressions, the translated code ensures that a branch is taken only if two of the condition computations agree on the branch. For function calls, the translated code ensures that, provided two of the three expressions denoting the function closure agree on a label, then the function denoted by this label is called.

The potential quadratic increase in code size, caused by the duplication of branch expressions, may be eliminated by replacing large branches with calls to thunkified code prior to the translation. In the case for function application, it is straightforward to avoid the duplication caused by repeated translation of the argument expression by introducing a let-binding construct for the argument.

$$\begin{aligned}
\llbracket \text{bool} \rrbracket_0 &= \text{bool} \\
\llbracket \text{nat} \rrbracket_0 &= \text{nat} \\
\llbracket \tau_1 \rightarrow \tau_2 \rrbracket_0 &= \llbracket \tau_1 \rrbracket_0 \rightarrow \llbracket \tau_2 \rrbracket_0 \\
\llbracket \tau \rrbracket &= \langle\langle \llbracket \tau \rrbracket_0, \llbracket \tau \rrbracket_0, \llbracket \tau \rrbracket_0 \rangle\rangle \\
\llbracket \Gamma \rrbracket \rho &= \Gamma' \\
&\text{where } \text{Dom}(\Gamma) = \text{Dom}(\rho) \text{ and} \\
&\quad \text{Dom}(\Gamma') = \text{fv}(\text{Ran}(\rho)) \text{ and} \\
&\quad \left((\Gamma(x) = \tau \text{ and } \rho(x) = (x_1, x_2, x_3)) \text{ implies} \right. \\
&\quad \left. \llbracket \tau \rrbracket = \langle \Gamma'(x_1), \Gamma'(x_2), \Gamma'(x_3) \rangle \right)
\end{aligned}$$

Figure 2. Translation of Types and Type Assumptions.

$$\begin{aligned}
\llbracket M \rrbracket &= M' \\
&\text{where } \text{Dom}(M) = \text{Dom}(M') \text{ and} \\
&\quad \left(M(l) = \lambda x. e \text{ implies} \right. \\
&\quad \left. M'(l) = \lambda \langle\langle x_1, x_2, x_3 \rangle\rangle. \llbracket e \rrbracket (x \mapsto (x_1, x_2, x_3)) \right) \\
\llbracket (M; e) \rrbracket \rho &= (\llbracket M \rrbracket; \llbracket e \rrbracket \rho)
\end{aligned}$$

Figure 3. Translation of Memories and Machine States.

4.1 Properties of the translation

For proving that the translation preserves typings (in a precise sense), we extend the translation to types as shown in Figure 2.

In what follows, extended language substitutions $e[vs/xs]$ are always on a form where $vs = \langle\langle v_1, v_2, v_3 \rangle\rangle$ and $xs = \langle\langle x_1, x_2, x_3 \rangle\rangle$, for some values v_1, v_2 , and v_3 , and some variables x_1, x_2 , and x_3 . Substitutions of this form are always defined and we shall use this property implicitly in the proofs.

We are now in the position to state a property saying that translation preserves typings:

PROPOSITION 10 (Translation Preserves Typing).

1. If $\Gamma \vdash^s e : \tau$ and $\Gamma' = \llbracket \Gamma \rrbracket \rho$ then $\Gamma' \vdash \llbracket e \rrbracket \rho : \llbracket \tau \rrbracket$.
2. If $\vdash^s S : \tau$ then $\vdash \llbracket S \rrbracket \varepsilon : \llbracket \tau \rrbracket$.

PROOF Part 1 can be proved using induction over the structure of e . Part 2 can be proved directly by unfolding the definitions of machine state typings and machine state translation, using part 1. \square

Notice that Proposition 10 also expresses that well-typed source language programs may be translated.

The translation is extended to memories M and machine states $(M; e)$, as shown in Figure 3.

PROPOSITION 11 If $S \rightarrow_0^* S'$ then $E[S] \rightarrow_0^* E[S']$.

PROOF Follows immediately by induction from (46), (48) and (49). \square

PROPOSITION 12 (Translation Closed Under Substitution). If $e' = \llbracket e \rrbracket (\rho, x \mapsto (x_1, x_2, x_3))$ then $e'[\langle\langle v, v, v \rangle\rangle / \langle\langle x_1, x_2, x_3 \rangle\rangle] = \llbracket e[v/x] \rrbracket \rho$.

PROOF By induction over the structure of e . \square

PROPOSITION 13 (Translation Substitution Invariance). *If $e' = \llbracket e \rrbracket \rho$ and $\{x_1, x_2, x_3\} \cap \text{fv}(\text{Ran}(\rho)) = \emptyset$, it holds for any v_1, v_2 , and v_3 that $e' \llbracket \langle \langle v_1, v_2, v_3 \rangle \rangle / \langle \langle x_1, x_2, x_3 \rangle \rangle \rrbracket = e'$.*

PROOF By induction over the structure of e . \square

We can now state a simulation proposition expressing that the translation of a source language program evaluates in alignment with the source language program. This property corresponds to the upper part of the diagram in Section 1, where \Rightarrow corresponds to translation in the empty environment:

$$\begin{array}{ccc} S & \hookrightarrow & S' \\ \Downarrow & & \Downarrow \\ S_0 & \xrightarrow{*} S_i & \xrightarrow{*} S_n \end{array}$$

The proposition is expressed as follows with $S_0 = \llbracket S \rrbracket \varepsilon$ and $S_n = \llbracket S' \rrbracket \varepsilon$:

PROPOSITION 14 (Simulation). *If $S \hookrightarrow S'$ and $\llbracket S \rrbracket \varepsilon$ and $\llbracket S' \rrbracket \varepsilon$ exist then $\llbracket S \rrbracket \varepsilon \xrightarrow{*} \llbracket S' \rrbracket \varepsilon$.*

PROOF By induction over the structure of e , where $S = (M; e)$ and $S' = (M'; e')$.

CASE $e = \text{true}$ It follows from (7) that $e' = \text{true}^*$. The result follows easily from the definition of translation, using (46) and (41) repeatedly.

CASE $e = \text{false}$ As the case for $e = \text{true}$.

CASE $e = n$ As the case for $e = \text{true}$.

CASE $e = \text{let } x = e_1 \text{ in } e_2$ There are two subcases. Either (9) is applied, in which case e_1 is a value v , or (1) is applied.

In the first subcase, we have $M' = M$ and $e' = e_2[v/x]$. We also have $\llbracket e \rrbracket \varepsilon = \text{let } \langle \langle x_1, x_2, x_3 \rangle \rangle = \langle \langle v, v, v \rangle \rangle \text{ in } \llbracket e_2 \rrbracket (x \mapsto (x_1, x_2, x_3))$. From (43), it follows that $\llbracket e \rrbracket \varepsilon \xrightarrow{0} \llbracket e_2 \rrbracket (x \mapsto (x_1, x_2, x_3)) \llbracket \langle \langle v, v, v \rangle \rangle / \langle \langle x_1, x_2, x_3 \rangle \rangle \rrbracket$. Further, from Proposition 12, we have $\llbracket e \rrbracket \varepsilon \xrightarrow{0} \llbracket e_2[v/x] \rrbracket \varepsilon$ and thus $\llbracket e \rrbracket \varepsilon \xrightarrow{0} \llbracket e' \rrbracket \varepsilon$. Finally, because $M = M'$, from assumptions and from (49), we have $\llbracket (M, e) \rrbracket \varepsilon \xrightarrow{*} \llbracket (M', e') \rrbracket \varepsilon$, as required.

In the second subcase, we have from (1) that there exists e'_1 such that $(M, e_1) \hookrightarrow (M', e'_1)$ and $e' = \text{let } x = e'_1 \text{ in } e_2$. By induction, we have $\llbracket (M, e_1) \rrbracket \varepsilon \xrightarrow{*} \llbracket (M', e'_1) \rrbracket \varepsilon$. We also have $\llbracket e \rrbracket \varepsilon = \text{let } \langle \langle x_1, x_2, x_3 \rangle \rangle = \llbracket e_1 \rrbracket \varepsilon \text{ in } \llbracket e_2 \rrbracket (x \mapsto (x_1, x_2, x_3))$. Because $\llbracket e' \rrbracket \varepsilon = \text{let } \langle \langle x_1, x_2, x_3 \rangle \rangle = \llbracket e'_1 \rrbracket \varepsilon \text{ in } \llbracket e_2 \rrbracket (x \mapsto (x_1, x_2, x_3))$, we can apply Proposition 11 to get $\llbracket (M, e) \rrbracket \varepsilon \xrightarrow{*} \llbracket (M', e') \rrbracket \varepsilon$, as required.

CASE $e = \text{if } e_b \text{ then } e_1 \text{ else } e_2$ There are three subcases to consider. Either $e_b = \text{true}^*$, or e_b is a value different from true^* or e_b is not a value.

In the first subcase, where $e_b = \text{true}^*$, rule (4) must have been applied, thus, we have $e' = e_1$ and $M' = M$. From the definition of translation, it follows that

$$\begin{aligned} \llbracket e \rrbracket \varepsilon &= \text{let } \langle \langle x_1, x_2, x_3 \rangle \rangle = \langle \langle \text{true}^*, \text{true}^*, \text{true}^* \rangle \rangle \\ &\text{in if } x_1 \triangleright r \text{ then} \\ &\quad \text{if } x_2 \triangleright g \text{ then } \llbracket e_1 \rrbracket \varepsilon \\ &\quad \text{else if } x_3 \triangleright b \text{ then } \llbracket e_1 \rrbracket \varepsilon \text{ else } \llbracket e_2 \rrbracket \varepsilon \\ &\text{else} \\ &\quad \text{if } x_2 \triangleright g \text{ then if } x_3 \triangleright b \text{ then } \llbracket e_1 \rrbracket \varepsilon \text{ else } \llbracket e_2 \rrbracket \varepsilon \\ &\quad \text{else } \llbracket e_2 \rrbracket \varepsilon \end{aligned}$$

Now, from Proposition 13, we have $(\llbracket e_1 \rrbracket \varepsilon) \llbracket vs / \langle \langle x_1, x_2, x_3 \rangle \rangle \rrbracket = \llbracket e_1 \rrbracket \varepsilon$ and $(\llbracket e_2 \rrbracket \varepsilon) \llbracket vs / \langle \langle x_1, x_2, x_3 \rangle \rangle \rrbracket$, for any $vs = \langle \langle v_1, v_2, v_3 \rangle \rangle$, thus, we can use (43) once and (38) twice to get $(\llbracket M \rrbracket, \llbracket e \rrbracket \varepsilon) \xrightarrow{*} (\llbracket M \rrbracket, \llbracket e_1 \rrbracket \varepsilon)$, as required.

In the second subcase, where e_b is a value different from true^* , rule (5) must have been applied, thus, we have $e' = e_2$ and $M' = M$. Following similar reasoning as in the previous subcase, but with (39) used three times, we get $(\llbracket M \rrbracket, \llbracket e \rrbracket \varepsilon) \xrightarrow{*} (\llbracket M \rrbracket, \llbracket e_2 \rrbracket \varepsilon)$, as required.

In the third subcase where e_b is not a value, rule (1) must have been applied, thus, there exists e'_b such that $(M, e_b) \hookrightarrow (M', e'_b)$ and $e' = \text{if } e'_b \text{ then } e_1 \text{ else } e_2$. By induction, we have $\llbracket (M, e_b) \rrbracket \varepsilon \xrightarrow{*} \llbracket (M', e'_b) \rrbracket \varepsilon$. We also have

$$\begin{aligned} \llbracket e \rrbracket \varepsilon &= \text{let } \langle \langle x_1, x_2, x_3 \rangle \rangle = \llbracket e_b \rrbracket \varepsilon \\ &\text{in if } x_1 \triangleright r \text{ then} \\ &\quad \text{if } x_2 \triangleright g \text{ then } \llbracket e_1 \rrbracket \varepsilon \\ &\quad \text{else if } x_3 \triangleright b \text{ then } \llbracket e_1 \rrbracket \varepsilon \text{ else } \llbracket e_2 \rrbracket \varepsilon \\ &\text{else} \\ &\quad \text{if } x_2 \triangleright g \text{ then if } x_3 \triangleright b \text{ then } \llbracket e_1 \rrbracket \varepsilon \text{ else } \llbracket e_2 \rrbracket \varepsilon \\ &\quad \text{else } \llbracket e_2 \rrbracket \varepsilon \end{aligned}$$

Because

$$\begin{aligned} \llbracket e' \rrbracket \varepsilon &= \text{let } \langle \langle x_1, x_2, x_3 \rangle \rangle = \llbracket e'_b \rrbracket \varepsilon \\ &\text{in if } x_1 \triangleright r \text{ then} \\ &\quad \text{if } x_2 \triangleright g \text{ then } \llbracket e_1 \rrbracket \varepsilon \\ &\quad \text{else if } x_3 \triangleright b \text{ then } \llbracket e_1 \rrbracket \varepsilon \text{ else } \llbracket e_2 \rrbracket \varepsilon \\ &\text{else} \\ &\quad \text{if } x_2 \triangleright g \text{ then if } x_3 \triangleright b \text{ then } \llbracket e_1 \rrbracket \varepsilon \text{ else } \llbracket e_2 \rrbracket \varepsilon \\ &\quad \text{else } \llbracket e_2 \rrbracket \varepsilon \end{aligned}$$

we can apply Proposition 11 to get $\llbracket (M, e) \rrbracket \varepsilon \xrightarrow{*} \llbracket (M', e') \rrbracket \varepsilon$, as required.

CASE $e = \lambda x. e_1$ It follows by inspection that (2) must have been applied, thus, we have $e' = l$ and $M' = (M, l \mapsto \lambda x. e_1)$. From the definition of translation, we have $\llbracket e \rrbracket \varepsilon = \lambda^{(r, g, b)} \langle \langle x_1, x_2, x_3 \rangle \rangle. \llbracket e_1 \rrbracket (x \mapsto (x_1, x_2, x_3))$. From (36), we have $\llbracket (M; e) \rrbracket \varepsilon \xrightarrow{0} (\llbracket M \rrbracket, l \mapsto \lambda \langle \langle x_1, x_2, x_3 \rangle \rangle. \llbracket e_1 \rrbracket (x \mapsto (x_1, x_2, x_3))) ; \langle \langle l, l, l \rangle \rangle$. It follows from the definition of M' and e' and from the definition of translation of machine states that $\llbracket (M; e) \rrbracket \varepsilon \xrightarrow{0} \llbracket (M', e') \rrbracket \varepsilon$. Now from (48), we have $\llbracket (M; e) \rrbracket \varepsilon \xrightarrow{*} \llbracket (M', e') \rrbracket \varepsilon$, as required.

CASE $e = e_1 e_2$ There are several subcases to consider. Either $e_1 = l$ and $e_2 = v$, in which case (3) is applied, or $e_1 = l$ and e_2 is a non-value expression, in which case (1) is applied, or both e_1 and e_2 are non-value expressions, in which case (1) is also applied.

We first consider the case where (3) is applied. In this case $M(l) = \lambda x. e_0$ and $e' = e_0[v/x]$ and $M' = M$. From the definition of translation, we have

$$\begin{aligned} \llbracket e \rrbracket \varepsilon &= \text{let } \langle \langle x_1, x_2, x_3 \rangle \rangle = \langle \langle l, l, l \rangle \rangle \\ &\text{in appe } [x_1 \triangleright r, x_2 \triangleright g] \langle \langle v, v, v \rangle \rangle \\ &\quad \text{else appe } [x_1 \triangleright r, x_3 \triangleright b] \langle \langle v, v, v \rangle \rangle \\ &\quad \text{else } (x_2 \triangleright g) \langle \langle v, v, v \rangle \rangle \end{aligned}$$

Using (43), it follows that we have

$$\begin{aligned} \llbracket M; e \rrbracket \varepsilon &\xrightarrow{0} \llbracket M \rrbracket ; \text{appe } [l \triangleright r, l \triangleright g] \langle \langle v, v, v \rangle \rangle \\ &\quad \text{else appe } [l \triangleright r, l \triangleright b] \langle \langle v, v, v \rangle \rangle \\ &\quad \text{else } (l \triangleright g) \langle \langle v, v, v \rangle \rangle \end{aligned}$$

Now, it also follows that $\llbracket M \rrbracket (l) = \lambda \langle \langle x_1, x_2, x_3 \rangle \rangle. \llbracket e_0 \rrbracket (x \mapsto (x_1, x_2, x_3))$. Thus, using (44), (48), and (49), it follows that $\llbracket M; e \rrbracket \varepsilon \xrightarrow{*} \llbracket M \rrbracket ; \llbracket e_0 \rrbracket (x \mapsto (x_1, x_2, x_3)) \llbracket \langle \langle v, v, v \rangle \rangle / \langle \langle x_1, x_2, x_3 \rangle \rangle \rrbracket$. Finally, from Proposition 12 and because $e' = e_0[v/x]$, we can conclude $\llbracket M; e \rrbracket \varepsilon \xrightarrow{*} \llbracket M'; e' \rrbracket \varepsilon$, as required.

The remaining subcases make use of contextual reasoning and are straightforward.

CASE $e = e_1 \text{ op } e_2$ The proof for this case is similar to the case for application. \square

Notice that Proposition 14 does not assume well-typedness of S and S' .

4.2 Fault Propagation

Before we can state a fault tolerance property for the system, we define a *fault propagation relation*, $S \succ_c S'$, where S and S' are machine states and c is a color. Provided S is a non-faulty machine state, S' denotes a machine state that may differ from S in that values decorated with the color c may differ in S and S' .

Machine State Fault Propagation

$$\boxed{S \succ_c S'}$$

$$\frac{M \succ_c M' \quad e \succ_c e'}{(M; e) \succ_c (M'; e')} \quad (68)$$

Memory Fault Propagation

$$\boxed{M \succ_c M'}$$

$$\frac{M \succ_c M' \quad e \succ_c e'}{(M, l \mapsto \lambda xs. e) \succ_c (M', l \mapsto \lambda xs. e')} \quad (69)$$

$$\frac{}{\varepsilon \succ_c \varepsilon} \quad (70)$$

Expression Fault Propagation

$$\boxed{e \succ_c e'}$$

$$\frac{}{x \triangleright c' \succ_c x \triangleright c'} \quad (71)$$

$$\frac{}{v \triangleright c \succ_c v' \triangleright c} \quad (72)$$

$$\frac{}{w \succ_c w} \quad (73)$$

$$\frac{}{n \triangleright c' \succ_c n \triangleright c'} \quad (74)$$

$$\frac{}{\text{true} \triangleright c' \succ_c \text{true} \triangleright c'} \quad (75)$$

$$\frac{}{\text{false} \triangleright c' \succ_c \text{false} \triangleright c'} \quad (76)$$

$$\frac{e \succ_c e'}{\lambda^{cs}.xs.e \succ_c \lambda^{cs}.xs.e'} \quad (77)$$

$$\frac{e \succ_c e' \quad e_1 \succ_c e'_1 \quad e_2 \succ_c e'_2}{\text{if } e \text{ then } e_1 \text{ else } e_2 \succ_c \text{if } e' \text{ then } e'_1 \text{ else } e'_2} \quad (78)$$

$$\frac{e_1 \succ_c e'_1 \quad e_2 \succ_c e'_2}{\text{let } xs = e_1 \text{ in } e_2 \succ_c \text{let } xs = e'_1 \text{ in } e'_2} \quad (79)$$

$$\frac{e_i \succ_c e'_i \quad i = 1..n}{\langle e_1, \dots, e_n \rangle \succ_c \langle e'_1, \dots, e'_n \rangle} \quad (80)$$

$$\frac{e_1 \succ_c e'_1 \quad e_2 \succ_c e'_2}{e_1 \text{ op}_{c'} e_2 \succ_c e'_1 \text{ op}_{c'} e'_2} \quad (81)$$

$$\frac{e_1 \succ_c e'_1 \quad e_2 \succ_c e'_2 \quad e_3 \succ_c e'_3 \quad e_4 \succ_c e'_4}{\text{appe } [e_1, e_2] e_3 \text{ else } e_4 \succ_c \text{appe } [e'_1, e'_2] e'_3 \text{ else } e'_4} \quad (82)$$

$$\frac{e_1 \succ_c e'_1 \quad e_2 \succ_c e'_2}{e_1 e_2 \succ_c e'_1 e'_2} \quad (83)$$

Notice that only rule (72) is particularly interesting. The remaining rules specify that the relation is closed under pair-wise simple induction on subexpressions.

When S and S' are machine states, we write $S \succ S'$ to mean that there exists c such that $S \succ_c S'$. The relation $S \succ S'$ expresses that S' may differ from S “only on one color”. Intuitively, after a transient fault, one of the colors have become faulty, but the remaining two colors can still be trusted. Notice that the relation \succ is reflexive and symmetric (we shall not make use of the property that \succ is symmetric).

The following proposition states that translation, substitution, and fault propagation interact in interesting ways:

PROPOSITION 15 (Fault Propagation Properties). *Let $\rho' = \rho, x \mapsto (x_1, x_2, x_3)$ and $xs = \langle x_1, x_2, x_3 \rangle$.*

1. $\llbracket e[v/x] \rrbracket \rho \succ_r \llbracket e \rrbracket \rho' [\langle v', v, v \rangle / xs]$
2. $\llbracket e[v/x] \rrbracket \rho \succ_g \llbracket e \rrbracket \rho' [\langle v, v', v \rangle / xs]$
3. $\llbracket e[v/x] \rrbracket \rho \succ_b \llbracket e \rrbracket \rho' [\langle v, v, v' \rangle / xs]$

PROOF By induction over the structure e . \square

We shall make use of yet another proposition, which states that once a color is considered faulty, more faults are allowed to be introduced on that color via substitutions:

PROPOSITION 16 (Fault Propagation Substitution). *Let $\rho' = \rho, x \mapsto (x_1, x_2, x_3)$ and $xs = \langle x_1, x_2, x_3 \rangle$.*

1. $\llbracket e \rrbracket \rho' \succ_r e' \Rightarrow \llbracket e[v/x] \rrbracket \rho \succ_r e' [\langle v', v, v \rangle / xs]$
2. $\llbracket e \rrbracket \rho' \succ_g e' \Rightarrow \llbracket e[v/x] \rrbracket \rho \succ_g e' [\langle v, v', v \rangle / xs]$
3. $\llbracket e \rrbracket \rho' \succ_b e' \Rightarrow \llbracket e[v/x] \rrbracket \rho \succ_b e' [\langle v, v, v' \rangle / xs]$

PROOF By induction over the structure e . \square

The proposition can be motivated by the following example, which shows that, due to substitutions, faults on a color may accumulate:

$$\begin{aligned} e &= \text{let } \langle y_1, y_2, y_3 \rangle = \langle v, v, v' \rangle \\ &\quad \text{in let } \langle x_1, x_2, x_3 \rangle = \langle y_1, y_2, y_3 \rangle \\ &\quad \quad \text{in } \langle x_1 + y_1, x_2 + y_2, x_3 + y_3 \rangle \\ \rightarrow_0 &\quad \text{let } \langle x_1, x_2, x_3 \rangle = \langle v, v, v' \rangle \\ &\quad \text{in } \langle x_1 + v, x_2 + v, x_3 + v' \rangle \\ \rightarrow_0 &\quad \langle v + v, v + v, v' + v' \rangle \end{aligned}$$

Here e is the initial expression in which a faulty value v' is being bound to y_3 and the non-faulty copies of the value v is being bound to y_1 and y_2 . By substitution, the single faulty-value is propagated to two different places. However, the notion of substitution disallows faults to propagate across colors.

The fault propagation relation is extended to evaluation contexts according to the rules below.

Evaluation Context Fault Propagation

$$\boxed{E \succ_c E'}$$

$$\frac{}{[\cdot] \succ_c [\cdot]} \quad (84)$$

$$\frac{E \succ_c E' \quad e \succ_c e'}{E e \succ_c E' e'} \quad (85)$$

$$\frac{w \succ_c w' \quad E \succ_c E'}{w E \succ_c w' E'} \quad (86)$$

$$\frac{E_1 \succ_c E_2 \quad e_1 \succ_c e_2 \quad e'_1 \succ_c e'_2 \quad e''_1 \succ_c e''_2}{\text{appe } [E_1, e_1] e'_1 \text{ else } e''_1 \succ_c \text{appe } [E_2, e_2] e'_2 \text{ else } e''_2} \quad (87)$$

$$\frac{w_1 \succ_c w_2 \quad E_1 \succ_c E_2 \quad e_1 \succ_c e_2 \quad e'_1 \succ_c e'_2}{\text{appe } [w_1, E_1] e_1 \text{ else } e'_1 \succ_c \text{appe } [w_2, E_2] e_2 \text{ else } e'_2} \quad (88)$$

$$\frac{w_1 \succ_c w_2 \quad w'_1 \succ_c w'_2 \quad E_1 \succ_c E_2 \quad e_1 \succ_c e_2}{\text{appe } [w_1, w'_1] E_1 \text{ else } e_1 \succ_c \text{appe } [w_2, w'_2] E_2 \text{ else } e_2} \quad (89)$$

$$\frac{E \succ_c E' \quad e \succ_c e'}{\text{let } xs = E \text{ in } e \succ_c \text{let } xs = E' \text{ in } e'} \quad (90)$$

$$\frac{E \succ_c E' \quad e_1 \succ_c e'_1 \quad e_2 \succ_c e'_2}{\text{if } E \text{ then } e_1 \text{ else } e_2 \succ_c \text{if } E' \text{ then } e'_1 \text{ else } e'_2} \quad (91)$$

$$\frac{E \succ_c E' \quad e \succ_c e'}{E \text{ op}_c e \succ_c E' \text{ op}_c e'} \quad (92)$$

$$\frac{w \succ_c w' \quad E \succ_c E'}{w \text{ op}_c E \succ_c w' \text{ op}_c E'} \quad (93)$$

$$\frac{w_i \succ_c w'_i, \quad i = [1..n] \quad E \succ_c E' \quad e_j \succ_c e'_j, \quad j = [1..m]}{\langle w_1, \dots, w_n, E, e_1, \dots, e_m \rangle \succ_c \langle w'_1, \dots, w'_n, E', e'_1, \dots, e'_m \rangle} \quad (94)$$

The following proposition expresses that the results of filling contexts is related by the fault propagation relation, provided the contexts and the filled-in objects are related:

PROPOSITION 17 (Fault Propagation Context Filling).
If $E \succ_c E'$ and $S \succ_c S'$ then $E[S] \succ_c E'[S']$.

PROOF By induction on the derivation $E \succ_c E'$. \square

The following proposition states that evaluation contexts are preserved by the fault propagation relation.

PROPOSITION 18 (Fault Propagation Context Preservation).
If $E[S] \succ_c S'$ then there exists E' and S'' such that $S \succ_c S''$ and $E \succ_c E'$ and $S' = E'[S'']$.

PROOF By induction on the structure of E . \square

The following proposition states that the translation is compositional with respect to context filling.

PROPOSITION 19 (Translation is Contextual Compositional).
If $S = \llbracket E[S_s] \rrbracket \varepsilon$ and $S' = \llbracket E[S'_s] \rrbracket \varepsilon$ then there exists E' such that $S = E'[\llbracket S_s \rrbracket \varepsilon]$ and $S' = E'[\llbracket S'_s \rrbracket \varepsilon]$.

PROOF By induction on the structure of E . We give the proof for one case below.

CASE $E = \text{let } x = E_0 \text{ in } e_0$ Let $S = (M; e)$ and $S' = (M'; e')$. From the definition of translation, we have

$$\begin{aligned} \llbracket E[S] \rrbracket \varepsilon &= (\llbracket M \rrbracket; \llbracket \text{let } x = E_0[e] \text{ in } e_0 \rrbracket \varepsilon) \\ &= (\llbracket M \rrbracket; \text{let } \langle x_1, x_2, x_3 \rangle = \llbracket E_0[e] \rrbracket \varepsilon \text{ in } \llbracket e_0 \rrbracket (x \mapsto (x_1, x_2, x_3))) \\ \llbracket E[S'] \rrbracket \varepsilon &= (\llbracket M' \rrbracket; \llbracket \text{let } x = E_0[e'] \text{ in } e_0 \rrbracket \varepsilon) \\ &= (\llbracket M' \rrbracket; \text{let } \langle x_1, x_2, x_3 \rangle = \llbracket E_0[e'] \rrbracket \varepsilon \text{ in } \llbracket e_0 \rrbracket (x \mapsto (x_1, x_2, x_3))) \end{aligned}$$

Let $S_0 = E_0[S]$ and $S'_0 = E_0[S']$. By induction, there exists E'_0 such that

$$\llbracket E_0[S] \rrbracket \varepsilon = E'_0[\llbracket S \rrbracket \varepsilon] \quad (95)$$

$$\llbracket E_0[S'] \rrbracket \varepsilon = E'_0[\llbracket S' \rrbracket \varepsilon] \quad (96)$$

Let

$$E' = \text{let } \langle x_1, x_2, x_3 \rangle = E'_0 \text{ in } \llbracket e_0 \rrbracket (x \mapsto (x_1, x_2, x_3)) \quad (97)$$

Using (95) and (97), we have

$$\begin{aligned} \llbracket E[S] \rrbracket \varepsilon &= (\llbracket M \rrbracket; \llbracket \text{let } x = E_0[e] \text{ in } e_0 \rrbracket \varepsilon) \\ &= (\llbracket M \rrbracket; \text{let } \langle x_1, x_2, x_3 \rangle = \llbracket E_0[e] \rrbracket \varepsilon \text{ in } \llbracket e_0 \rrbracket (x \mapsto (x_1, x_2, x_3))) \\ &= (\llbracket M \rrbracket; \text{let } \langle x_1, x_2, x_3 \rangle = E'_0[\llbracket e \rrbracket \varepsilon] \text{ in } \llbracket e_0 \rrbracket (x \mapsto (x_1, x_2, x_3))) \\ &= (\llbracket M \rrbracket; E'[\llbracket e \rrbracket \varepsilon]) \\ &= E'[\llbracket S \rrbracket \varepsilon] \end{aligned}$$

as required. Similarly, using (96), we can also, as required, derive $\llbracket E[S'] \rrbracket \varepsilon = E'[\llbracket S' \rrbracket \varepsilon]$ \square

4.3 Single Step Fault Tolerance

The following proposition states that whenever a transient fault occurs during execution of an extended-language program, the resulting machine state is related (via the fault propagation relation) to the corresponding non-faulty machine state:

PROPOSITION 20 (Faults Are Captured by Fault Propagation).
If $S \rightarrow_1 S_0^z$ then $S \succ_c S^z$.

PROOF By straightforward induction over E , using (47). \square

Now, consider again the diagram from Section 1:

$$\begin{array}{ccccc} S & \hookrightarrow & S' & & \\ \downarrow & & \downarrow & & \\ S_0 & \xrightarrow{*} & S_i & \xrightarrow{*} & S_n \\ & & \downarrow & & \Upsilon \\ & & S^z & \xrightarrow{*} & S_n^z \end{array}$$

We have already covered the upper part of this diagram with Proposition 14, which states that a translated program simulates the source language program. We are now in the position to express the heart of the fault tolerance property, which allows the target-level program to tolerate a single transient fault during execution.

The proposition expresses that the fault propagation relation on machine states is closed under evaluation. In terms of the machine states in the diagram, if a fault occurs at machine state S_i , enforcing the program into a machine state S^z , then, assuming no additional faults, S^z may evaluate into a machine state S_n^z , which is related to the source language machine state S' via voting.

PROPOSITION 21 (Single Step Fault Tolerance). Assume $S \hookrightarrow S'$ and $\llbracket S \rrbracket \varepsilon \xrightarrow{*} S_0 \xrightarrow{*} \llbracket S' \rrbracket \varepsilon$.

For any S_0^z and color c such that $S_0 \succ_c S_0^z$ there exists S^z such that $S_0^z \xrightarrow{*} S^z$ and $\llbracket S' \rrbracket \varepsilon \succ_c S^z$.

PROOF By induction over the derivation $S \hookrightarrow S'$, where $S = (M; e_0)$.

CASE $S = (M; \text{true})$ For this case we know (7) is applied. We have $\llbracket S \rrbracket \varepsilon = (\llbracket M \rrbracket; e)$, where $e = \langle \text{true}, \text{true}, \text{true} \rangle$

and $S' = (M; \text{true}^*)$ and $\llbracket S' \rrbracket \varepsilon = (\llbracket M \rrbracket; e')$, where $e' = \langle\langle \text{true}^*, \text{true}^*, \text{true}^* \rangle\rangle$. We also have

$$\llbracket S \rrbracket \varepsilon \rightarrow_0 S_1 \rightarrow_0 S_2 \rightarrow_0 \llbracket S' \rrbracket \varepsilon$$

where

$$\begin{aligned} S_1 &= (M; e_1) & \text{and} & & e_1 &= \langle\langle \text{true}^*, \text{true}, \text{true} \rangle\rangle \\ S_2 &= (M; e_2) & \text{and} & & e_2 &= \langle\langle \text{true}^*, \text{true}^*, \text{true} \rangle\rangle \end{aligned}$$

There are now four subcases to consider:

SUBCASE $S_0 = \llbracket S \rrbracket \varepsilon$: From the definition of \succ_c , we have $S_0^z = (M^z; e)$ — there are no values to zap in e . Let $S_1^z = (M^z; e')$. We then have from repeated use of (46), (41), and (48) that $S_0^z \rightarrow_0^* S_1^z$. Moreover, from the definition of \succ it follows trivially that $\llbracket S' \rrbracket \varepsilon \succ S_1^z$, as required.

SUBCASE $S_0 = S_1$: From the definition of \succ_c , we have $\llbracket M \rrbracket \succ_c M_0^z$ and $e_1 \succ_c e_0^z$, where $S_0^z = (M_0^z; e_0^z)$. We now proceed by case analysis of c .

First consider the case where $c = r$. From the definition of \succ_c , we have $e_0^z = \langle\langle v, \text{true}, \text{true} \rangle\rangle$ for some v . It follows that we have $S_0^z \rightarrow_0^* S_1^z$, where $S_1^z = (M_0^z; e_1^z)$ and $e_1^z = \langle\langle v, \text{true}^*, \text{true} \rangle\rangle$. Moreover, $\llbracket S' \rrbracket \varepsilon \succ_c S_1^z$, as required.

Alternatively, we have $c = g$ or $c = b$. From the definition of \succ_c , we then have $e_0^z = e_1$. It follows that $S_0^z \rightarrow_0^* S_1^z$, where $S_1^z = (M_0^z; e')$. Moreover, from the definition of \succ_c , we have $\llbracket S' \rrbracket \varepsilon \succ_c S_1^z$, as required.

SUBCASE $S_0 = S_2$: From the definition of \succ_c , we have $\llbracket M \rrbracket \succ_c M_0^z$ and $e_2 \succ_c e_0^z$, where $S_0^z = (M_0^z; e_0^z)$. We now proceed by case analysis of c .

First assume $c = r$. From the definition of \succ_c , we have $e_0^z = \langle\langle v, \text{true}^*, \text{true} \rangle\rangle$ for some v . It follows that we have $S_0^z \rightarrow_0^* S_1^z$, where $S_1^z = (M_0^z; e_1^z)$ and $e_1^z = \langle\langle v, \text{true}^*, \text{true}^* \rangle\rangle$. Moreover, $\llbracket S' \rrbracket \varepsilon \succ_c S_1^z$ holds, as required.

Second, assume $c = g$. The result follows similarly as for the case $c = r$, but with $e_0^z = \langle\langle \text{true}^*, v, \text{true}^* \rangle\rangle$ for some v and $e_1^z = \langle\langle \text{true}^*, v, \text{true}^* \rangle\rangle$.

Finally, assume $c = b$. From the definition of \succ_c , we have $e_0^z = e_2$. It follows that $S_0^z \rightarrow_0^* S_1^z$, where $S_1^z = (M_0^z; e')$. Moreover, from the definition of \succ_c , we have $\llbracket S' \rrbracket \varepsilon \succ_c S_1^z$, as required.

SUBCASE $S_0 = \llbracket S' \rrbracket \varepsilon$: Trivially true with $S_1^z = S_0^z$.

CASE $S = (M; \text{let } x = v \text{ in } e_s)$ For this case we know (9) is applied. Let $xs = \langle\langle x_1, x_2, x_3 \rangle\rangle$ and $vs = \langle\langle v, v, v \rangle\rangle$. We have $\llbracket S \rrbracket \varepsilon = (\llbracket M \rrbracket; e)$, where

$$e = \text{let } xs = vs \text{ in } \llbracket e_s \rrbracket (x \mapsto (x_1, x_2, x_3))$$

$$S' = (M; e_s[v/x])$$

$$\llbracket S' \rrbracket \varepsilon = (\llbracket M \rrbracket; e') \tag{98}$$

$$e' = \llbracket e_s[v/x] \rrbracket \varepsilon \tag{99}$$

Using Proposition 12, we also have

$$\begin{aligned} \llbracket S \rrbracket \varepsilon &\rightarrow_0 (\llbracket M \rrbracket; \llbracket e_s \rrbracket (x \mapsto (x_1, x_2, x_3)))[vs/xs] \\ &= (\llbracket M \rrbracket; e') = \llbracket S' \rrbracket \varepsilon \end{aligned}$$

Thus there are two subcases to consider.

SUBCASE $S_0 = \llbracket S \rrbracket \varepsilon$: From the definition of \succ_c , we have

$$\llbracket M \rrbracket \succ_c M_0^z \tag{100}$$

and $e \succ_c e_0^z$ and $S_0 = (M_0^z; e_0^z)$. We now proceed by case analysis of c .

First, consider the case $c = r$. From the definition of \succ_r , we have

$$e_0^z = \text{let } xs = vs' \text{ in } e_s^z$$

where

$$\llbracket e_s \rrbracket (x \mapsto (x_1, x_2, x_3)) \succ_r e_s^z \tag{101}$$

and $vs' = \langle\langle v', v, v \rangle\rangle$ for arbitrary v' . From (43), there exists S_1^z such that

$$S_0^z \rightarrow_0 S_1^z \tag{102}$$

where $S_1^z = (M_0^z; e_1^z)$ and $e_1^z = e_s^z[vs'/xs]$. From Proposition 16(1) and (101), we have $\llbracket e_s[v/x] \rrbracket \varepsilon \succ_r e_1^z$. Thus, from (99), we have

$$e' \succ_r e_1^z \tag{103}$$

From the definition of \succ_c and from (103), (98), and (100), we have $\llbracket S' \rrbracket \varepsilon \succ_c S_1^z$. Moreover, from (102) and (48), we also have $S_0^z \rightarrow_0^* S_1^z$, as required.

The cases for $c = g$ and $c = b$ are similar to the case for $c = r$.

SUBCASE $S_0 = \llbracket S' \rrbracket \varepsilon$: Follows trivially with $S_1^z = S_0^z$ using (49).

CASE $S = (M; l \ v)$ For this case, we know (3) is applied. Let $xs = \langle\langle x_1, x_2, x_3 \rangle\rangle$. We have $\llbracket S \rrbracket \varepsilon = (\llbracket M \rrbracket; e)$, where

$$\begin{aligned} e &= \text{let } xs = \llbracket l \rrbracket \varepsilon \\ &\text{in } \text{appe } [x_1 \triangleright r, x_2 \triangleright g] (\llbracket v \rrbracket \varepsilon) \\ &\quad \text{else } \text{appe } [x_1 \triangleright r, x_3 \triangleright b] (\llbracket v \rrbracket \varepsilon) \\ &\quad \text{else } (x_2 \triangleright g) (\llbracket v \rrbracket \varepsilon) \end{aligned}$$

and $M(l) = \lambda x. e^0$. From (43), we have $(\llbracket M \rrbracket; e) \rightarrow_0 S_1$, where

$$S_1 = (\llbracket M \rrbracket; e_1) \tag{104}$$

and

$$\begin{aligned} e_1 &= \text{appe } [l \triangleright r, l \triangleright g] (\llbracket v \rrbracket \varepsilon) \\ &\quad \text{else } \text{appe } [l \triangleright r, l \triangleright b] (\llbracket v \rrbracket \varepsilon) \\ &\quad \text{else } (l \triangleright g) (\llbracket v \rrbracket \varepsilon) \end{aligned}$$

Moreover, from (44), we have $S_1 \rightarrow_0 S_2$, where $S_2 = (\llbracket M \rrbracket; e_2)$ and

$$e_2 = \llbracket e^0 \rrbracket (x \mapsto (x_1, x_2, x_3))[vs/xs] \tag{105}$$

$$\llbracket M \rrbracket(l) = \lambda xs. \llbracket e^0 \rrbracket (x \mapsto (x_1, x_2, x_3)) \tag{106}$$

$$vs = \langle\langle v, v, v \rangle\rangle$$

We also have from (3) that

$$S' = (M; e^0[v/x]) \tag{107}$$

Using Proposition 16(1), we have $\llbracket S' \rrbracket \varepsilon = S_2$. Thus, we have

$$\llbracket S \rrbracket \varepsilon \rightarrow_0 S_1 \rightarrow_0 \llbracket S' \rrbracket \varepsilon$$

which leads to three subcases to consider for S_0 , namely $S_0 = \llbracket S \rrbracket \varepsilon$ or $S_0 = S_1$ or $S_0 = \llbracket S' \rrbracket \varepsilon$.

SUBCASE $S_0 = \llbracket S \rrbracket \varepsilon$: From the definition of \succ_c , we have $S_0^z = (M_0^z; e_0^z)$, where

$$\llbracket M \rrbracket \succ_c M_0^z \tag{108}$$

$$e \succ_c e_0^z \tag{109}$$

We proceed by case analysis of c .

[SUBSUBCASE $c = r$:] From the definition of \succ_r and from (109), we have

$$\begin{aligned} e_0^z &= \text{let } xs = \langle\langle v_1, l, l \rangle\rangle \\ &\text{in } \text{appe } [x_1 \triangleright r, x_2 \triangleright g] \langle\langle v_2, v, v \rangle\rangle \\ &\quad \text{else } \text{appe } [x_1 \triangleright r, x_3 \triangleright b] \langle\langle v_3, v, v \rangle\rangle \\ &\quad \text{else } (x_2 \triangleright g) \langle\langle v_4, v, v \rangle\rangle \end{aligned}$$

for arbitrary v_1, v_2, v_3 , and v_4 . Now, from (43), we have

$$S_0^z \rightarrow_0 S_1^z \quad (110)$$

$$S_1^z = (M_0^z; e_1^z) \quad (111)$$

and

$$e_1^z = \begin{array}{l} \text{appe } [v_1 \triangleright r, l \triangleright g] \langle \langle v_2, v, v \rangle \rangle \\ \text{else } \text{appe } [v_1 \triangleright r, l \triangleright b] \langle \langle v_3, v, v \rangle \rangle \\ \quad \text{else } (l \triangleright g) \langle \langle v_4, v, v \rangle \rangle \end{array} \quad (112)$$

From (106), (108), and from the definition of \succ_c , we have there exists e_2^z such that

$$M_0^z(l) = \lambda x s. e_z^0 \quad (113)$$

$$\llbracket e^0 \rrbracket (x \mapsto (x_1, x_2, x_3)) \succ_r e_z^0 \quad (114)$$

There are now two cases; either $v_1 = l$ or $v_1 \neq l$.

We first consider the case for $v_1 = l$. From (111), (112), (113), and (44), we have

$$S_1^z \rightarrow_0 S_2^z \quad (115)$$

$$S_2^z = (M_0^z; e_2^z) \quad (116)$$

$$e_2^z = e_z^0[\langle \langle v_2, v, v \rangle \rangle / xs] \quad (117)$$

Now, from Proposition 16, (114), and (117), we have

$$\llbracket e^0[v/x] \rrbracket \varepsilon \succ_r e_2^z \quad (118)$$

From (107), (118), (115), (116), (110), (108), and (48), we have there exists $S^z = S_2^z$ such that $S_0^z \rightarrow_0^* S^z$ and $\llbracket S' \rrbracket \varepsilon \succ_c S^z$, as required.

We now consider the case for $v_1 \neq l$. From (112), (111), and from using (45) twice, we have

$$S_1^z \rightarrow_0 S_2^z \rightarrow_0 S_3^z \quad (119)$$

$$S_3^z = (M_0^z; e_3^z) \quad (120)$$

$$e_3^z = (l \triangleright g) \langle \langle v_4, v, v \rangle \rangle \quad (121)$$

From (120), (121), (113), and from (37), we have

$$S_3^z \rightarrow_0 S_4^z \quad (122)$$

$$S_4^z = (M_0^z; e_4^z) \quad (123)$$

$$e_4^z = e_z^0[\langle \langle v_4, v, v \rangle \rangle / xs] \quad (123)$$

Now, from Proposition 16, (114), and (123), we have

$$\llbracket e^0[v/x] \rrbracket \varepsilon \succ_r e_4^z \quad (124)$$

From (107), (124), (110), (119), (122), (108), and (48), we have there exists $S^z = S_4^z$ such that $S_0^z \rightarrow_0^* S^z$ and $\llbracket S' \rrbracket \varepsilon \succ_c S^z$, as required.

[SUBSUBCASE $c = g$ or $c = b$:] These cases are similar to the case for $c = r$.

SUBCASE $S_0 = S_1$: From the definition of \succ_c and from (104), we have $S_0^z = (M_0^z; e_0^z)$, where

$$\llbracket M \rrbracket \succ_c M_0^z \quad (125)$$

$$e_1 \succ_c e_0^z \quad (126)$$

We now proceed by case analysis of c .

[SUBSUBCASE $c = r$:] From the definition of \succ_r , we have

$$e_0^z = \begin{array}{l} \text{appe } [v_1 \triangleright r, l \triangleright g] \langle \langle v_2, v, v \rangle \rangle \\ \text{else } \text{appe } [v_3 \triangleright r, l \triangleright b] \langle \langle v_4, v, v \rangle \rangle \\ \quad \text{else } (l \triangleright g) \langle \langle v_5, v, v \rangle \rangle \end{array}$$

for arbitrary v_1, v_2, v_3, v_4 , and v_5 . The remainder of this case follows the lines of the subcase $S_0 = \llbracket S \rrbracket \varepsilon$ with independent treatment of cases for whether $v_1 = l$ and whether $v_3 = l$.

[SUBSUBCASE $c = g$ or $c = b$:] These cases are similar to the case for $c = r$.

SUBCASE $S_0 = \llbracket S' \rrbracket \varepsilon$: The proof for this case follows immediately from reflexivity of \rightarrow_0^* and \succ_c .

CASE $S = (M; \lambda x. e^0)$ For this case we know (2) is applied. We have $\llbracket S \rrbracket \varepsilon = (\llbracket M \rrbracket; e)$, where

$$e = \lambda^{(r,g,b)} x s. \llbracket e^0 \rrbracket (x \mapsto (x_1, x_2, x_3))$$

and $x s = \langle \langle x_1, x_2, x_3 \rangle \rangle$. From (36), we have $\llbracket S \rrbracket \varepsilon \rightarrow_0 (M'; \langle \langle l, l, l \rangle \rangle)$, where

$$M' = \llbracket M \rrbracket, l \mapsto \lambda x s. \llbracket e^0 \rrbracket (x \mapsto (x_1, x_2, x_3)) \quad (127)$$

From assumptions and (2), we have $S' = (M, l \mapsto \lambda x. e^0; l)$. It follows from the definition of translation of memory maps that

$$\llbracket S' \rrbracket \varepsilon = (M'; \langle \langle l, l, l \rangle \rangle) \quad (128)$$

thus, we have $\llbracket S \rrbracket \varepsilon \rightarrow_0 \llbracket S' \rrbracket \varepsilon$, which means that there are two cases to consider for S_0 , namely $S_0 = \llbracket S \rrbracket \varepsilon$ and $S_0 = \llbracket S' \rrbracket \varepsilon$.

SUBCASE $S_0 = \llbracket S \rrbracket \varepsilon$: From the definition of \succ_c , we have $S_0 \succ_c S^z$, where

$$S_0^z = (M_0^z; e_0^z) \quad (129)$$

$$\llbracket M \rrbracket \succ_c M_0^z \quad (130)$$

$$e_0^z = \lambda^{(r,g,b)} x s. e_z^0 \quad (131)$$

$$\llbracket e^0 \rrbracket (x \mapsto (x_1, x_2, x_3)) \succ_c e_z^0 \quad (132)$$

From (36), (129), and (131), we have

$$S_0^z \rightarrow_0 (M_1^z; e_1^z) \quad (133)$$

where

$$M_1^z = M_0^z, l \mapsto \lambda x s. e_z^0 \quad (134)$$

$$e_1^z = \langle \langle l, l, l \rangle \rangle \quad (135)$$

From the definition of \succ_c and from the definition of translation and from (130), (134), (127), and (132), we have

$$M' \succ_c M_1^z \quad (136)$$

Moreover, from reflexivity of \succ_c , from (48), and from (136) and (128), there exists $S^z = (M_1^z; e_1^z)$ such that $S_0^z \rightarrow_0^* S^z$ and $\llbracket S' \rrbracket \varepsilon \succ_c S^z$, as required.

SUBCASE $S_0 = \llbracket S' \rrbracket \varepsilon$: The proof for this subcase follows immediately from reflexivity of \rightarrow_0^* and \succ_c .

CASE $S = (M; \text{if } e \text{ then } e_1 \text{ else } e_2)$ The proof for this case is similar to, but simpler than, the case for application.

CASE $S = (M; n_1 * + n_2 *)$ For this case, we know that (10) is applied, thus, we have

$$S' = (M'; n *) \quad (137)$$

$$n = n_1 + n_2 \quad (138)$$

From the definition of translation, we have

$$\llbracket S \rrbracket \varepsilon = (\llbracket M \rrbracket; e) \quad (139)$$

$$e = \text{let } x s = \langle \langle n_1 *, n_1 *, n_1 * \rangle \rangle \quad (140)$$

$$\text{in let } y s = \langle \langle n_2 *, n_2 *, n_2 * \rangle \rangle \\ \text{in } ((x_1 \triangleright r) +_r (y_1 \triangleright r), \\ (x_2 \triangleright g) +_g (y_2 \triangleright g), \\ (x_3 \triangleright b) +_b (y_3 \triangleright b))$$

$$x s = \langle \langle x_1, x_2, x_3 \rangle \rangle$$

$$y s = \langle \langle y_1, y_2, y_3 \rangle \rangle$$

for distinct choices of $x_1, x_2, x_3, y_1, y_2,$ and y_3 .

From (43), (46), (31), and from (139), (140), (137), and (138), we have

$$\llbracket S \rrbracket \varepsilon \rightarrow_0 S_1 \rightarrow_0 S_2 \rightarrow_0 S_3 \rightarrow_0 S_4 \rightarrow_0 \llbracket S' \rrbracket \varepsilon \quad (141)$$

where $S_1 = (\llbracket M \rrbracket; e_1)$ and $S_2 = (\llbracket M \rrbracket; e_2)$ and $S_3 = (\llbracket M \rrbracket; e_3)$ and $S_4 = (\llbracket M \rrbracket; e_4)$ and $\llbracket S' \rrbracket \varepsilon = (\llbracket M \rrbracket; \llbracket n^* \rrbracket \varepsilon)$ and

$$e_1 = \text{let } ys = \langle \langle n_2^*, n_2^*, n_2^* \rangle \rangle \quad (142)$$

$$\text{in } \langle (n_1^* \triangleright r) \text{+}_r (y_1 \triangleright r), \quad (143)$$

$$(n_1^* \triangleright g) \text{+}_g (y_2 \triangleright g), \quad (144)$$

$$(n_1^* \triangleright b) \text{+}_b (y_3 \triangleright b) \rangle$$

$$e_2 = \langle (n_1^* \triangleright r) \text{+}_r (n_2^* \triangleright r), \quad (143)$$

$$(n_1^* \triangleright g) \text{+}_g (n_2^* \triangleright g), \quad (144)$$

$$(n_1^* \triangleright b) \text{+}_b (n_2^* \triangleright b) \rangle$$

$$e_3 = \langle n^*, \quad (144)$$

$$(n_1^* \triangleright g) \text{+}_g (n_2^* \triangleright g), \quad (145)$$

$$(n_1^* \triangleright b) \text{+}_b (n_2^* \triangleright b) \rangle$$

$$e_4 = \langle n^*, \quad (145)$$

$$n^*, \quad (146)$$

$$(n_1^* \triangleright b) \text{+}_b (n_2^* \triangleright b) \rangle$$

Thus, there are six subcases to check, namely $S_0 = \llbracket S \rrbracket \varepsilon$, $S_0 = S_1$, $S_0 = S_2$, $S_0 = S_3$, $S_0 = S_4$, and $S_0 = \llbracket S' \rrbracket \varepsilon$. The case for $S_0 = \llbracket S' \rrbracket \varepsilon$ follows trivially due to reflexivity of \succ_c . We show one of the remaining proof cases.

SUBCASE $S_0 = S_3$: From the definition of \succ_c , we have

$$S_0^z = (M_0^z; e_0^z) \quad (146)$$

$$\llbracket M \rrbracket \succ_c M_0^z \quad (147)$$

$$e_3 \succ_c e_0^z \quad (148)$$

We now proceed by case analysis of c and show the case for $c = g$ below; the remaining cases are similar.

[SUBSUBCASE $c = g$:] It follows that

$$e_0^z = \langle n^* \triangleright r, \quad (149)$$

$$(v_2 \triangleright g) \text{+}_g (v_2 \triangleright g), \quad (150)$$

$$(n_1^* \triangleright b) \text{+}_b (n_2^* \triangleright b) \rangle$$

for arbitrary v_1 and v_2 . There are now two cases; either v_1 and v_2 are both numbers or one of v_1 and v_2 is not a number.

Case $v_1 = n_1^*$ and $v_2 = n_2^*$ for some n_1' and n_2' : It follows from (31), (46), (149), and (146) that

$$S_0^z \rightarrow_0 S_1^z \quad (150)$$

$$S_1^z = (M_0^z; e_1^z) \quad (151)$$

$$e_0^z = \langle n^* \triangleright r, \quad (152)$$

$$n_1^* \triangleright g, \quad (153)$$

$$(n_1^* \triangleright b) \text{+}_b (n_2^* \triangleright b) \rangle$$

$$n' = n_1' + n_2' \quad (153)$$

Applying (31) and (46) again, we get

$$S_1^z \rightarrow_0 S_2^z \quad (154)$$

$$S_2^z = (M_0^z; e_2^z) \quad (155)$$

$$e_2^z = \langle \langle n^*, n_1^*, n_2^* \rangle \rangle \quad (156)$$

From (147), (156), (155), (137), (154), (150), and (48), there exists $S^z = S_2^z$ such that $S_0 \rightarrow_0^* S^z$ and $\llbracket S' \rrbracket \varepsilon \succ_c S^z$, as required.

Case $v_1 \neq n_1^*$ or $v_2 \neq n_2^*$ for some n_1' and n_2' : It follows from (32) and from (46) and from (149) and (146) that

$$S_0^z \rightarrow_0 S_1^z \quad (157)$$

$$S_1^z = (M_0^z; e_1^z) \quad (158)$$

$$e_1^z = \langle n^*, \quad (159)$$

$$v, \quad (160)$$

$$(n_1^* \triangleright b) \text{+}_b (n_2^* \triangleright b) \rangle$$

for some v . Applying (31) and (46) using (159) and (158) yields

$$S_1^z \rightarrow_0 S_2^z \quad (160)$$

$$S_2^z = (M_0^z; e_2^z) \quad (161)$$

$$e_2^z = \langle \langle n^*, v, n^* \rangle \rangle \quad (162)$$

Now, from (147), (162), (161), (137), (160), (157), and (48), there exists $S^z = S_2^z$ such that $S_0 \rightarrow_0^* S^z$ and $\llbracket S' \rrbracket \varepsilon \succ_c S^z$, as required.

CASE $S = (M; E[e_s])$; rule (46) From assumptions, we have

$$S_s \hookrightarrow S'_s \quad (163)$$

$$S = E[S_s] \quad (164)$$

$$S' = E[S'_s] \quad (165)$$

where $S' = (M'; E[e'_s])$ and $S_s = (M; e_s)$ and $S'_s = (M; e'_s)$. From assumptions, (164), (165), and Proposition 19, we have there exists E' such that

$$E'[\llbracket S_s \rrbracket \varepsilon] = \llbracket E[S_s] \rrbracket \varepsilon = \llbracket S \rrbracket \varepsilon \quad (166)$$

$$E'[\llbracket S'_s \rrbracket \varepsilon] = \llbracket E[S'_s] \rrbracket \varepsilon = \llbracket S' \rrbracket \varepsilon \quad (167)$$

Now, because $\llbracket S_s \rrbracket \varepsilon$ and $\llbracket S'_s \rrbracket \varepsilon$ exist, we can apply Proposition 14, using (163), to get

$$\llbracket S_s \rrbracket \varepsilon \rightarrow_0^* \llbracket S'_s \rrbracket \varepsilon \quad (168)$$

From (166), (167), and assumptions, we have

$$E'[\llbracket S_s \rrbracket \varepsilon] \rightarrow_0^* S_0 \rightarrow_0^* E'[\llbracket S'_s \rrbracket \varepsilon] \quad (169)$$

From (168) and repeated use of Proposition 11, we have

$$E'[\llbracket S_s \rrbracket \varepsilon] \rightarrow_0 E'[S_1] \rightarrow_0 \dots \rightarrow_0 E'[S_n] \rightarrow_0 E'[\llbracket S'_s \rrbracket \varepsilon] \quad (170)$$

$$\llbracket S_s \rrbracket \varepsilon \rightarrow_0 S_1 \rightarrow_0 \dots \rightarrow_0 S_n \rightarrow_0 \llbracket S'_s \rrbracket \varepsilon \quad (171)$$

Now, because evaluation is deterministic (Proposition 9) and by comparing (169) and (170), we have, using (171), there exists S'_0 such that

$$S_0 = E'[S'_0] \quad (172)$$

$$\llbracket S_s \rrbracket \varepsilon \rightarrow_0^* S'_0 \rightarrow_0^* \llbracket S'_s \rrbracket \varepsilon \quad (173)$$

Now, let S_0^z be any machine state and c any color $r, g,$ or b such that

$$S_0 \succ_c S_0^z \quad (174)$$

From (174), (172), and Proposition 18, we have there exist E'' and S_0^u such that

$$S_0^z = E''[S_0^u] \quad (175)$$

$$S'_0 \succ_c S_0^u \quad (176)$$

$$E' \succ_c E'' \quad (177)$$

From (163) and (173), it follows by induction, using (176), that there exists S^u such that

$$S_0^u \rightarrow_0^* S^u \quad (178)$$

$$\llbracket S'_s \rrbracket \varepsilon \succ_c S^u \quad (179)$$

Let

$$S^z = E''[S^u] \quad (180)$$

From Proposition 11, (178), (175), and (180), we have $S_0^z \rightarrow_0^* S^z$, as required. Moreover, from (177), (167), (179), (180), and Proposition 17, we also have $\llbracket S' \rrbracket_\varepsilon \succ_c S^z$, as required. \square

4.4 Multi-Step Fault Tolerance

We can now use the fault-tolerance proposition from Section 4.3 to yield the following “multi-step” fault-tolerance property:

PROPOSITION 22 (Multi-Step Fault-Tolerance). *Assume $S \hookrightarrow^* S'$ and $\llbracket S \rrbracket_\varepsilon \rightarrow_0^* S_0 \rightarrow_0^* \llbracket S' \rrbracket_\varepsilon$.*

For any S_0^z such that $S_0 \succ S_0^z$ there exists S^z such that $S_0^z \rightarrow_0^ S^z$ and $\llbracket S' \rrbracket_\varepsilon \succ S^z$.*

PROOF By induction on the length of the derivation $S \hookrightarrow^* S'$, using Proposition 21. \square

Intuitively, this proposition is obtained by composing diagrams of the form shown in Section 4.3.

4.5 Putting it All Together

An important feature of the fault propagation relation is that it supports voting when used together with the faulty translation. We define *voting* as a binary relation \approx_{vote} between source language terminal machine states and target language terminal machine states.

$$\begin{aligned} (M; v) &\approx_{\text{vote}} (M'; \langle\langle v', v, v \rangle\rangle) && \text{if } \llbracket M \rrbracket \succ_r M' \\ (M; v) &\approx_{\text{vote}} (M'; \langle\langle v, v', v \rangle\rangle) && \text{if } \llbracket M \rrbracket \succ_g M' \\ (M; v) &\approx_{\text{vote}} (M'; \langle\langle v, v, v' \rangle\rangle) && \text{if } \llbracket M \rrbracket \succ_b M' \end{aligned}$$

The following property holds:

PROPOSITION 23 (Fault Propagation Supports Voting). *If $\llbracket S_v \rrbracket_\varepsilon \succ S_v^z$ then $S_v \approx_{\text{vote}} S_v^z$.*

PROOF Follows immediately from the definition of translation of values. \square

We can now tie all the previous properties together in the following top-level fault-tolerance property:

THEOREM 1 (Top-level Fault Tolerance). *Assume $\vdash^s S : \tau$ and $k \leq 1$.*

1. *Either there exists S_v such that $S \hookrightarrow^* S_v$ in which case there exists S_v^z such that $\llbracket S \rrbracket_\varepsilon \rightarrow_k^* S_v^z$ and for all such S_v^z it holds that $S_v \approx_{\text{vote}} S_v^z$.*
2. *Otherwise $S \uparrow$ in which case $\llbracket S \rrbracket_\varepsilon \uparrow_k$.*

PROOF From Proposition 4, it follows that either there exists S_v such that $S \hookrightarrow^* S_v$ or $S \uparrow$.

We now consider part one of the proposition, with $k = 1$. From assumptions and from Proposition 10, we have $\llbracket S \rrbracket_\varepsilon$ exists. From assumptions and repeated use of Proposition 6, we have $\vdash^s S_v : \tau$, thus, from Proposition 10, we have $\llbracket S' \rrbracket_\varepsilon$ exists. Now, let S_v^z be any terminal machine state such that

$$\llbracket S \rrbracket_\varepsilon \rightarrow_k^* S_v^z \quad (181)$$

From (181) and due to the property that non-faulty evaluation is deterministic (Proposition 9), there exist S_0 and S_0^z such that

$$\llbracket S \rrbracket_\varepsilon \rightarrow_0^* S_0 \rightarrow_0^* \llbracket S_v \rrbracket_\varepsilon \quad (182)$$

$$S_0 \rightarrow_1 S_0^z \quad (183)$$

$$S_0^z \rightarrow_0^* S_v^z$$

From (183) and Proposition 20, we have

$$S_0 \succ S_0^z \quad (184)$$

Now, from assumptions, (182), (184), and Proposition 22, there exists S^z such that

$$\llbracket S_v \rrbracket_\varepsilon \succ S^z \quad (185)$$

From (185) and Proposition 23, we have $S_v \approx_{\text{vote}} S_v^z$, as required.

For $k = 0$, part one of the proposition is similar but simpler than the case for $k = 1$.

For $k = 0$, part two of the proposition follows immediately from Proposition 14. For $k = 1$, part two follows from Proposition 14 and from Proposition 21. \square

Notice that in the proof of Proposition 1, we make essential use of the property that non-faulty evaluation is deterministic (Proposition 9). It is thus important that machine states are considered identical up-to consistent renaming of labels.

5. Conclusion and Future Work

We have presented a simply typed lambda calculus extended with two simple atomic operations that allows the calculus to be the target of a fault-tolerant translation that allows single-transient faults to appear at runtime, without effecting the overall result of evaluation.

There are plenty of possibilities for future work. First, the scheme does not support *full fault recovery* in the sense that after a fault, the system can recover to a state where it is ready for a new arbitrary fault. This problem is also present in previous work [18]. A weaker *partial recovery* property may be easier to establish. Let two function invocations be *unrelated* if none of the function invocations makes a function call that directly results in the other function invocation. A partial fault recovery property may state that as long as two transient faults occur during execution of unrelated function invocations then execution can proceed safely. Such a partial fault recovery property may be implemented by majority voting on function arguments and results and on storing closure environments (i.e., on substitutions under lambda-bindings in our formulation).

Second, another interesting direction for future work would be to investigate techniques for supporting fault-tolerant control registers, perhaps through bootstrapping [3] a faulty-translated interpreter written in an appropriately extended version of the source language. Similarly, by writing a garbage collector in such a source language, the faulty translation may result in a fault-tolerant garbage collector for use by the system.

Third, another interesting direction for future work is to investigate type systems for capturing the soundness of the embedded voting instructions, which could be a first step towards a lower-level type safe language for full transient-fault tolerant computing [8].

Also identified in [18] as possible future work is to investigate the possibilities for optimizations of generated fault-tolerant code. Whereas simple in-lining, constant propagation, and constant folding appear to be sound optimizations, common subexpression elimination across colors is not sound in general.

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